Current Progress of Si/Graphene Nanocomposites for Lithium-Ion Batteries

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Abstract: The demand for high performance lithium-ion batteries (LIBs) is increasing due to widespread use of portable devices and electric vehicles. Silicon (Si) is one of the most attractive candidate anode materials for next generation LIBs. However, the high-volume change (>300%) during lithium ion alloying/de-alloying leads to poor cycle life. When Si is used as the anode, conductive carbon is needed to provide the necessary conductivity. However, the traditional carbon coating method could not overcome the challenges of pulverization and unstable Solid Electrolyte Interphase (SEI) layer during long-term cycling. Since 2010, Si/Graphene composites have been vigorously studied in hopes of providing a material with better cycling performance. This paper reviews current progress of Si/Graphene nanocomposites in LIBs. Different fabrication methods have been studied to synthesize Si/Graphene nanocomposites with promising electrochemical performances. Graphene plays a key enabling role in Si/Graphene anodes. However, the desired properties of graphene for this application have not been systematically studied and understood. Further systematic investigation of the desired graphene properties is suggested to better control the Si/Graphene anode performance.

Keywords: Si anode; graphene; nanocomposites; lithium-ion batteries

1. Introduction

Carbon coating has been vigorously studied for many energy sources such as solar cells, fuel cells and LIBs [1–3]. The carbon coating method has proven itself as simple and efficient for property enhancement. Typically, carbon precursors are introduced to the surface of active material and calcinated at an elevated temperature to acquire the final product coated by layers of carbon. For example, anatase TiO2 with carbon coating has shown enhanced photoactivity under UV irradiation [4]. Carbon coating on the SiC nanowires (NWs) in SiC matrix composite has been used to increase the fracture toughness and flexural strength [5]. Ti bipolar plates coated with amorphous carbon for polymer electrolyte membrane fuel cells (PEMFC) have been observed to demonstrate lower contact resistance and higher output power than that of non-coated Ti bipolar plates [6].

Lithium-ion battery (LIB) has become the prevailing power supply for portable electronic devices since its first commercialization in 1991 by Sony [7]. LIB offers several advantages including high energy density, good charging rate, long cycle life and high-power retention [8–10]. In addition, LIB is currently the most popular alternative option to replace the non-renewable fossil fuel for powering vehicles [11,12]. High capacity, fast charging rate and low cost are demanded by customers. Developing new electrode materials for next generation LIBs remains a challenge.
Currently, Si is one of the most attractive candidate anode materials for next generation LIBs. The commonly used graphite anode provides a capacity of 372 mAh/g at its highest lithiated state of LiC₆ [13]. Si can provide a capacity of 3578 mAh/g by forming Li₁₅Si₄ at room temperature [14]. It also has the theoretical highest capacity of 4200 mAh/g by forming Li₂₂Si₅ at elevated temperatures [15]. In addition to its 10 times capacity increase when compared to graphite, Si also has a lower, more favorable discharging potential of about 0.3 V vs. Li/Li⁺, which is very close to lithium metal [14,16]. This low potential results in a high-power battery since the greater voltage difference between cathode and anode, the more power the full cell can deliver. Furthermore, Si ranks as the 2nd most abundant element in Earth’s crust next to oxygen [17] and it is environmental friendly.

The main challenges that prevent Si-based anode from replacing graphite anode are the huge volume expansion during lithiation processes, slow lithium diffusion rate, and low electronic conductivity. In fact, the volume of lithiated Li₁₅Si₄ is 370% of delithiated Si [18]. There are two primary approaches to overcome the consequences of the structural collapse due to volume change, nanotechnology and carbon coating. Nanosized Si-based anode exhibits improved cycle life and high rate stability compared with bulk Si-based anode [19,20]. Nanosized Si-based anode has a very high surface area, which allows for low charge transfer resistance and benefits the rate of the lithiation and delithiation processes. The fast lithium ion transportation rate can lead to an improved high rate performance and reduce the pulverization. In Si-based anode, carbon coating provides an electron pathway to loose Si clusters. Nanosized Si in combination with different forms of carbon materials have been explored for high performance Si/C nanocomposite anode. These carbon forms include 0-D (nanodot and nanoparticle) [15], 1-D (nanowire and nanotube) [21], 2-D (graphene and thin film) [22,23] and 3-D (porous and hierarchical) [24,25] structures. The representing cycling performances of various reported Si/C anodes are given in Table 1. Among them, Si/Graphene nanocomposite has demonstrated great potential.

Since 2010, the number of scientific publications on Si/Graphene anode has been steadily increasing according to the Web of Science search results shown in Figure 1. It is obvious that the number of published articles with topics on “Silicon + Graphene + Anode” increased quickly over the years. The search in Google Scholar using keywords “Silicon + Graphene + Anode + Lithium + Battery” resulted in a much larger number of publications in a similar trend. Given the interest and amount of work done in this material, a review on the state of the art of Si/Graphene nanocomposite anode is certainly desirable. Through our review, it was clear that even though much effort has been investing in the synthesis of Si/Graphene nanocomposite anodes, the exact working mechanism of graphene in this composite electrode has not yet been carefully studied. A better understanding of the desirable properties of Graphene in this composite electrode will benefit future efforts on Si/Graphene nanocomposite anodes.

![Figure 1. Number of publications in Web of Science with topic on “Silicon + Anode + Graphene” from 2010 to 2017.](image-url)
Table 1. Selected examples of Si anode with different carbon forms.

<table>
<thead>
<tr>
<th></th>
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<th></th>
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</tr>
</thead>
<tbody>
<tr>
<td>Carbon Black</td>
<td>Si NPs</td>
<td>a-Si</td>
<td>Si NPs</td>
<td>Si NPs</td>
</tr>
<tr>
<td>CNFs</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Graphene</td>
<td></td>
<td></td>
<td>Hierarchical Carbon Frame</td>
<td></td>
</tr>
<tr>
<td>Si Source</td>
<td>Si NPs</td>
<td>a-Si</td>
<td>Si NPs</td>
<td>Si NPs</td>
</tr>
<tr>
<td>Si Loading</td>
<td>60%</td>
<td>75%</td>
<td>70%</td>
<td>50%</td>
</tr>
<tr>
<td>Initial Capacity (mAh/g)</td>
<td>~3000</td>
<td>~2000</td>
<td>~2300</td>
<td>~2000</td>
</tr>
<tr>
<td>Cycling Performance (mAh/g)</td>
<td>~1500</td>
<td>~1500</td>
<td>~1700</td>
<td>~1600</td>
</tr>
<tr>
<td></td>
<td>22 cycles</td>
<td>55 cycles</td>
<td>120 cycles</td>
<td>100 cycles</td>
</tr>
</tbody>
</table>

2. Si and Graphene in Electrodes

2.1. Si Electrochemistry

The properties of typical anode materials for LIB are summarized in Table 2. Si clearly outperforms on many fronts. However, despite decades of intensive study, a commercialized full cell with a Si anode providing desirable rate and cycling performance still remains challenging. There are serious consequences besides the physical structure collapse due to volume expansion resulted from lithiating of Si, such as: (1) solid electrolyte interphase (SEI) layer becomes unstable after several cycles of lithiation and delithiation leading to short cycle life and pulverization; (2) active Si anode loses contact with conductive carbon and binder resulting in permanent capacity fading; and (3) Si anode performs poorly at high charging/discharging rate due to the low electron conductivity.

Table 2. Comparison of anode materials.

<table>
<thead>
<tr>
<th>Anode</th>
<th>Li Metal [26]</th>
<th>Graphite</th>
<th>Silicon</th>
</tr>
</thead>
<tbody>
<tr>
<td>Potential vs. Lithium</td>
<td>0</td>
<td>~0.15</td>
<td>~0.3</td>
</tr>
<tr>
<td>Theoretical Capacity (mAh/g)</td>
<td>3860</td>
<td>372 (LiC₆)</td>
<td>3578 (Li₁₅Si₄)</td>
</tr>
<tr>
<td>Typical Charging Rate</td>
<td>&gt;1 C</td>
<td>&gt;1 C</td>
<td>1/10 C</td>
</tr>
<tr>
<td>Reported Cycle Life</td>
<td>300</td>
<td>&gt;1000</td>
<td>100~200</td>
</tr>
<tr>
<td>Ion Storage Mechanism</td>
<td>Plating/Stripping</td>
<td>Insertion/Extraction</td>
<td>Alloying/De-alloying</td>
</tr>
<tr>
<td>Current status</td>
<td>Research</td>
<td>Commercialized</td>
<td>Research</td>
</tr>
</tbody>
</table>

Typical charging/discharging curves for first two cycles of Si anode are presented in Figure 2. There are four plateaus labeled as Equations (1)–(4) corresponding to the following reactions [27]:

\[
\text{Si (crystalline)} + x \text{ Li} \rightarrow \left( 1 - \frac{x}{y} \right) \text{Si} \quad + \quad \frac{x}{y} \text{Li}_y \text{Si} \quad \text{(amorphous)} \quad (1)
\]

\[
4 \text{Li}_y \text{Si} \quad \text{(amorphous)} + (15 - 4y) \text{Li} \leftrightarrow \text{Li}_{15} \text{Si}_4 \quad \text{(crystalline)} \quad \text{at} \quad V < 60 \text{ mV} \quad (2a)
\]

\[
\text{Li}_{15} \text{Si}_4 \quad \text{(crystalline)} \leftrightarrow 4 \text{Li}_y \text{Si} \quad \text{(amorphous)} + (15 - 4y) \text{Li} \quad \text{at} \quad V < 60 \text{ mV} \quad (2b)
\]

\[
\text{Li}_{15} \text{Si}_4 \quad \text{(amorphous)} \rightarrow 4 \text{Si} \quad \text{(amorphous)} + 15 \text{Li} \quad (3)
\]

\[
\text{Si} \quad \text{(amorphous)} + x \text{ Li} \rightarrow \left( 1 - \frac{x}{y} \right) \text{Si} \quad \text{(amorphous)} \quad + \quad \frac{x}{y} \text{Li}_y \text{Si} \quad \text{(amorphous)} \quad (4)
\]

The first discharging reaction includes a process in which crystalline Si transfers into amorphous Si as confirmed by Chiang et al. [27]. When the Si-based anode undergoes the first constant current discharging, the potential drops rapidly from initial state of open circuit current down to around 0.1 V vs. Li/Li⁺. Then a broad and flat plateau appears between 0.08 V and 0.12 V vs. Li/Li⁺, which indicates that the crystalline Si is under lithiating, and is transferring into amorphous LiₙSi as shown in Equation (1). The full capacity of Si (3570 mAh/g, Li₁₅Si₄) can be reached when the potential drops to 0 V vs. Li/Li⁺ as the amorphous LiₙSi transfers into crystalline Li₁₅Si₄ (Equation (2a)).
This recrystallization begins at the potential of 60 mV vs. Li/Li$.^+$. During the first constant current charging process, the crystalline Li$_{15}$Si$_4$ breaks into small clusters of amorphous Li$_{15}$Si$_4$ as seen in Equation (2b). Then the chemical potential continuously increases up to about 0.4 V vs. Li/Li$^+$ as crystalline Li$_{15}$Si$_4$ transfers into amorphous Li$_{15}$Si$_4$ gradually. At the potential of 0.4 V vs. Li/Li$^+$, amorphous Si is delithiated according to Equation (3) as indicated by a plateau. During the second constant current discharging, the potential drops to ~0.3 V vs. Li/Li$^+$ then reaches a plateau that is resulted from amorphous Si transferring into amorphous Li$_4$Si (Equation (4)). At the end of the second discharging, the slope of plateau changes again because of the formation of crystalline Li$_{15}$Si$_4$ starting at potential of 60 mV vs. Li/Li$^+$ as seen in Equation (2a).

![Figure 2. A typical charging/discharging curve for Si anode (~100 nm Si electrode, Polyvinylidene fluoride (PVDF) and Carbon Black; weight ratio 7:2:1, no further modification).](image)

Given Si’s huge volume expansion during lithiation and its low electric conductivity, cracking of Si anode during cycling is the main issue that leads to battery failure. A Si thin film was used as the substrate to observe the cracking behavior during charging/discharging as reported by Wang et al. [28]. A large-area nano-textured silicon thin films (NTSTF) was prepared and the 1st lithiation process resulted in dendrite-like cracks on the surface. After the 1st delithiation process, the cracking remained visible by Scanning Electron Microscopy (SEM). Due to the stress generated by charging/discharging process, the thin film eventually peeled off from its substrate and completely lost electronic contact after about 10 cycles. Several coating materials including Cu, Al$_2$O$_3$ and Ti were utilized with 2–5 nm thickness to restrain the cracks [28,29] and each of them helped reduce the crack size, control the crack elongation and improve the electrochemical performance. These experiments agreed with the results from other coating methods, especially carbon coating.

2.2. Graphene in Electrodes

Carbon coating has been applied to improve conductivity and electrochemical performance in Si/C composite anode since the 1990s [30]. Carbon coating is needed to provide good electric conductivity in Si/C matrix. Traditional carbon coating methods include polymer decomposition [31], chemical/atomic deposition [32] and mechanical milling [33]. Two challenges persist in attempts to obtain long cycle life for Si anode. Firstly, due to the volume expansion and contraction of Si during lithiation/delithiation, pulverization happens and results in loose Si particles that stop participating in electrochemical reactions. Secondly, the solid electrolyte interphase (SEI) layer breaks during each charging cycle, and new SEI layers grow subsequently. This repetitive process consumes both Si and electrolyte. The traditional carbon coating on Si has not been able to sufficiently overcome these two challenges [34–36].
Thus, graphene was considered as an alternative carbon coating with the hope for graphene to retain intimate contact with Si nanoparticles before and after volume expansion, be electronically and ionically conductive, and be able to maintain stable SEI layers [37,38].

Graphene, a 2-D single layer of bonded carbon atoms was first synthesized by Novoselov and Geim in 2004 [39] through mechanical exfoliation method on bulk graphite flask. Graphene exhibited exceptional physical properties, such as high electrical conductivity (10^6 S/m) [40,41], high thermal conductivity (500–600 W/(m·K)) [42] and super stiffness (1.1 × 10^3 GPa Young’s Modulus) [40]. Its unique physical properties can be attributed to its C=C resonance structure, hexagonally arranged sp² σ bonding and π electron. Several synthesis methods have been developed, including exfoliation of graphite [43,44], chemical vapor deposition (CVD) [45] and thermal/chemical reduce of graphite oxide (GO) [41,44]. The microstructure, chemical and physical properties of the graphene are highly dependent on their synthesis routines. For example, the micromechanical cleavage method (scotch tape method) yields high quality graphene mono-/bi-sheets with the least number of defects [46]; mono-layer graphene grown by CVD method on the Cu substrate could achieve up to 0.76 m in diagonal length which is suitable for field emission applications with good ohmic contact [47,48]; and graphene obtained by unzipping carbon nanotubes can create a band-gap which is different from the typical graphene with zero band-gap [49,50].

Among all the methods, graphene synthesized by GO is commonly used in electrochemical devices such as photovoltaic cells, super capacitors and LIBs [51–53]. The GO reduction is a process of low cost, simplicity and relatively high yield. The process enables functionalization of the graphene to provide specific properties demanded by certain applications. Although how graphene acts in composite electrodes has not been fully investigated, graphene is typically considered as not reactive with most of the current electrodes, electrolyte, and separator materials [54]. For example, a direct electron transfer glucose biosensor fabricated by self-assembling of glucose oxidase and reduced carboxyl graphene exhibited a linear response to glucose concentrations; A three-dimensional hierarchical architecture Li/S battery was achieved by sandwiching multiwall carbon nanotube/sulfur (MWCNT@S) composite with amine functional graphene nanosheets [55].

Graphene can serve either as an electrode material or an additive material in LIBs. Until the cost of synthesizing graphene becomes more affordable, the use of graphene as additive material seems to be applicable. The improvement of electrochemical performance as a result of adding graphene has been reported in many anode materials such as carbon based graphene composite, metal oxide/graphene composite and alloy with graphene [56–59].

3. Si/Graphene Anodes

A summary of reported studies on Si/Graphene nanocomposites is provided in Table 3 with unified units in current rate and capacity; this is discussed in the following sections.
Table 3. A summary of studies on Si/Graphene nanocomposites anodes.

<table>
<thead>
<tr>
<th>Si Source</th>
<th>Graphene Synthesis Method</th>
<th>Graphene Weight Ratio</th>
<th>Electrode Loading Density (mg/cm²)</th>
<th>Initial Reversible Capacity (mAh/g)</th>
<th>First Cycle Efficiency</th>
<th>Best Capacity Retention with Current Rate</th>
<th>Ref. with Similar Si/Graphene Structure</th>
</tr>
</thead>
<tbody>
<tr>
<td>Si NPs</td>
<td>Graphite Oxidation + Thermal Reduction</td>
<td>~40%</td>
<td>2</td>
<td>~2050</td>
<td>~96%</td>
<td>300th, ~56%, 1000 mA/g</td>
<td>[23, 60]</td>
</tr>
<tr>
<td>Si NPs</td>
<td>Modified Hummers Method + Chemical Reduction</td>
<td>73.6%</td>
<td>NR</td>
<td>~1000</td>
<td>~41%</td>
<td>100th, ~70.8%, 50 mA/g</td>
<td>[61, 62]</td>
</tr>
<tr>
<td>Si NPs</td>
<td>Hummers Method + Thermal Reduction</td>
<td>66.7%</td>
<td>NR</td>
<td>1040</td>
<td>63%</td>
<td>30th, 94%, 50 mA/g</td>
<td>[63–67]</td>
</tr>
<tr>
<td>Aerosol Droplets</td>
<td>Crumpled Reduction</td>
<td>40%</td>
<td>0.2</td>
<td>1175</td>
<td>~95%</td>
<td>250th, ~86%, 1000 mA/g</td>
<td>[70, 71]</td>
</tr>
<tr>
<td>3-D Porous Si by Magnesiothermic</td>
<td>Hummers Method + Thermal Reduction</td>
<td>~40%</td>
<td>NR</td>
<td>1100</td>
<td>~79%</td>
<td>100th, ~50%, 5000 mA/g</td>
<td>[72–76]</td>
</tr>
<tr>
<td>Si NPs on graphite Foam</td>
<td>Hummers Method + Thermal Reduction</td>
<td>15.1%</td>
<td>1.5</td>
<td>1000</td>
<td>62.5%</td>
<td>100th, ~37%, 400 mA/g</td>
<td>[77]</td>
</tr>
<tr>
<td>Si NPs on 3-D tree-like GNS</td>
<td>Microwave Plasma CVD</td>
<td>19%</td>
<td>NR</td>
<td>2731</td>
<td>~56%</td>
<td>160th, ~67%, 150 mA/g</td>
<td>[78]</td>
</tr>
<tr>
<td>(PANI)-Si NPs</td>
<td>Modified Hummers Method + Pyrolysis Method</td>
<td>26%</td>
<td>0.3</td>
<td>~1500</td>
<td>~70%</td>
<td>300th, ~76%, 2000 mA/g</td>
<td>[79]</td>
</tr>
<tr>
<td>Si NPs on Electrospun CNFs</td>
<td>Chemical Method + Thermal Reduction</td>
<td>0.6%</td>
<td>NR</td>
<td>1270</td>
<td>71.2%</td>
<td>50th, ~91%, 100 mA/g</td>
<td>[80]</td>
</tr>
<tr>
<td>Si NPs on Graphene Hydrogel</td>
<td>Modified Hummers Method Ascorbic Acid + Thermal Reduction</td>
<td>29%</td>
<td>NR</td>
<td>2250</td>
<td>53%</td>
<td>150th, ~50%, 100 mA/g</td>
<td>[81]</td>
</tr>
<tr>
<td>3D Si NWs</td>
<td>CVD + Plasma enhanced CVD</td>
<td>NR</td>
<td>~0.5</td>
<td>~2600</td>
<td>97%</td>
<td>100th, ~29%, 500 mA/g</td>
<td>[82]</td>
</tr>
</tbody>
</table>
3.1. Various Synthesis Methods to Form Si/Graphene Composite Anode

After the successful demonstration of graphene supported SnO$_2$ nanoparticles (NPs) as LIB anode [58], Kung et al. was the first group to report Si NPs-graphene composites for LIB anodes in the year 2010 [23]. In this study, Si NPs with particle size less than 30 nm were pre-treated in air overnight to ensure the formation of silicon oxide layer on the surface and then mixed with graphene oxide (GO) synthesized by graphite oxidization. The mixture was reduced by 10% H$_2$ in Ar at elevated temperature to form the Si/Graphene nanocomposites. The as-fabricated Si/Graphene nanocomposites exhibited a capacity of approximately 1950 mAh/g in the first discharging process and about 900 mAh/g capacity retention after 120 cycles at a current rate of 1000 mA/g [23]. Later, Wang et al. demonstrated a free-standing graphene-Si composite film with graphene reduced by Hydrazine at mild elevated temperature ($80^\circ$C) and the Si particle size ranged from 3 to 80 nm [61]. It showed an excellent cycling performance with only 3% capacity loss from the fifth cycle (~820 mAh/g) to the 100th cycle (~800 mAh/g). Besides the Si NPs, there were other forms of Si/Graphene composites. Yan et al. were able to grow Si nanowires (SNWs) on graphene surface by using Au NPs as catalysts. This study reported an initial capacity of ~3500 mAh/g and a cycling performance of above 1500 mAh/g after 30 cycles at the current rate of 420 mA/g [83]. Liu and Zhu et al. fabricated a 3-D porous architecture of Si/Graphene nanocomposite where 3-D Si was synthesized by the Magnesiothermic Reduction method. This 3-D porous Si/Graphene nanocomposite demonstrated a capacity retention of more than 400 mAh/g at the high current rate of 5000 mA/g after 100 cycles [72]. In these pioneer studies, the preparation of Si/Graphene nanocomposites relied on dispersion, mixing and catalyst-assisted growth. The electrochemical performance of those nanocomposites depended highly on dispersion of nanostructured Si and large graphene loading (more than 30 wt % as shown in Table 3).

In 2012, Huang et al. used evaporation-induced capillary force to wrap graphene sheets around the Si NPs, where graphene was heavily crumpled to form the shell structure. Though the weight ratio of graphene was high (40 wt %), it was the first attempt to form capsules of Si NPs with graphene. This crumpled graphene-Si nanocomposite showed an initial capacity of 1175 mAh/g and 86% capacity retention after 250 cycles. The crumpled graphene structure was believed to help accommodate the expansion/contraction of encapsulated Si without fracture [70]. In 2013, Ji and Ruoff et al. fabricated a graphene-encapsulated Si on Ultrathin-Graphite Foam (UGF) for LIB anode. In their work, Si NPs were first modified by poly(diallyldimethylammonium chloride) (PDDA) to have graphene oxide (GO) wrapping. The resulted Si/GO NPs were then drop-casted on UGF surface, followed with a thermal annealing process to obtain Si/Graphene NPs on UGF. The initial capacity of Si/Graphene NPs on UGF was around 1000 mAh/g and the retained capacity was above 400 mAh/g after 100 cycles at the current rate of 400 mA/g [77]. Li and Zhi et al. demonstrated a self-supporting binder-free Si based anode by encapsulation of Si NWs with dual adaptable apparels. First, overlapped graphene sheets were grown from Si NWs by the Chemical Vapor Deposition (CVD) method to form Si NWs@G nanocables. The resulted powders were dispersed in graphene oxide aqueous solution before vacuum filtration. Finally, the graphene oxide was reduced to obtain SiNWs@graphene@reduced-graphene-oxide structure for accommodation of volume expansion during lithiation. Scanning transmission electron microscopy (STEM) did not identify any loose Si clusters in this composite after initial lithiation process. The fabricated SiNWs@graphene@reduced-graphene-oxide composite demonstrated excellent high rate cycling performance. It had a >95% capacity retention for 50 cycles at the current rate of 840 mA/g and >90% capacity retention for 100 cycles at the current rate of 2100 mA/g [84].

Another technique to fabricate Si/Graphene nanocomposite was pyrolysis that began with freeze-drying of Si/GO suspensions. After the pyrolysis process, GO reduction at elevated temperature was needed to obtain sandwiched Si/Graphene [65,79]. When the loading ratio of Si NPs and graphene was 1:2, the composites demonstrated good cycling performance. The capacity at the 200th cycle was ~600 mAh/g (96% initial capacity retention).
3.2. Composition of Si/Graphene Composite Anode

Many studies focused on the synthesizing method of Si/Graphene anode and used high graphene ratio (>20 wt %) [65,75,80]. The effect of increased graphene weight ratio in Si/Graphene nanocomposite was investigated by Hwang et al. Si/Graphene with weight ratio of 3:1, 2:1, 1:1 and 1:2 were studied. The Si/graphene with ratio of 3:1 demonstrated the best balance on both initial capacity and cycling performance [75]. Chabot et al. also investigated the loading ratio between Si NPs and graphene. Si/Graphene nanocomposites with ratio of 1:3, 1:2, 1:1 and 1:0.5 were studied. Si/Graphene with ratio of 1:0.5 had the highest initial capacity of ~2600 mAh/g. However, Si/Graphene with ratio of 1:1 exhibited the highest retention capacity of ~1000 mAh/g after 100 cycles [65].

The electrode loading density is also very important since it could impact the whole cell energy density [85,86]. A loading density of 2.5~3.5 mAh/cm$^2$ (equal to ~1 mg/cm$^2$ of Si anode) is the minimum requirement in the traditional full cells, and a 7~10 mg/cm$^2$ loading density is desirable for high energy density battery applications [87,88]. Low loading density of the anode undesirably increases the total weight of the full cell and brings down the overall energy density, which is unfavorable for electric vehicles (EVs) [89]. As seen in Table 3, many of the published reports did not reveal the electrode loading density data. Only a couple of studies reported loading densities meeting the requirement for commercial full cells. Ji and Ruoff et al. demonstrated a graphene-encapsulated Si on ultrathin-graphite foam (UGF) anode with electrode loading density of 1.5 mg/cm$^2$. The overall initial capacity of the Si/Graphene/UGF electrode was 983 mAh/g, and the retained capacity at 100th cycle was 370 mAh/g [77]. Kung fabricated Si nanoparticles-graphene paper composite anode with electrode loading density of 2 mg/cm$^2$. An initial capacity of ~2000 mAh/g was achieved with cycling performance of ~1500 mAh/g at the 300th cycle [23].

3.3. Graphene Quality

It is well established that the quality of graphene highly depends on the synthesis conditions when fabricated by reduction of GO. For example, the history of thermal process in Hummers’ method heavily impacted the defect density in graphene nanosheets [90,91]. Some et al. obtained Nitrogen/Sulphur-impurity-free graphene by thiophene template-assist chemical reduction with thermal healing process [92]. Our previous work demonstrated that the quality of synthesized graphene nanosheets was strongly dependent on the processing conditions [93]. However, the effect of graphene quality on the performance of battery electrodes has yet to be investigated systematically.

SEM was typically the first option for most scientists to observe the morphology of Si/Graphene composites. However, the thickness and number of layers of Graphene cannot be identified due to the limitation of SEM imaging. For example, a crumple surface of graphene sheet was visualized by SEM [89,94]. It was speculated that the wrinkled surface morphology indicated a relatively high surface area of graphene and contributed to the increase in electron conductivity, which resulted in an improved electrochemical performance [70,95]. The transmission electron microscope (TEM) was applied to investigate the crystal structure of Si/Graphene nanocomposites. By adjusting the contract of TEM image, graphene sheet can be isolated from the Si/Graphene nanocomposites due to its thin thickness and light atomic weight. The atomic force microscope (AFM) was utilized to measure the thickness of graphene nanosheets. Many of the fabricated Si/Graphene nanocomposites had graphene thickness of less than 10 nm [23,83,94]. However, AFM was not able to tell if the graphene was single layer due to molecules absorbed on the graphene surface.

Raman spectrum was efficient to provide additional information about the quality of the synthesized graphene nanosheet/nanosheets [95–97]. It had been well established that graphite and graphene have characteristic peaks at 1340 cm$^{-1}$, 1584 cm$^{-1}$ and 2700 cm$^{-1}$ as shown in Figure 3. The peak at 1340 cm$^{-1}$ was characterized as D band. The peak at 1584 cm$^{-1}$ was called G band, which was caused by E$_{2g}$ vibration mode that represented the C=C bond stretching of all pairs of sp$^2$ atoms. The peak at 2700 cm$^{-1}$ was characterized as G’ (2D) band, which was caused by the double resonance process. The positions of both G and G’ bands could determine the number of layers of...
graphene nanosheets. For a single graphene, the peak position would have 5 cm$^{-1}$ upshifting with constant intensity. The $G'$ band was also named 2D band because the $G'$ peak would follow the movement of D peak with a correlated number of layers of graphene nanosheets. There were actually two individual peaks identified within the 2D band, which were 2D$_1$ and 2D$_2$ in graphite, but only 2D$_1$ could be observed in single layer graphene. The intensity of 2D$_2$ peak increased with the increasing number of graphene layers. The 2D peak eventually grew into the typical graphite characteristic peaks when there were more than five layers of graphene nanosheets stacked together [96,98]. The Raman spectrum could also provide information about the size of graphene flakes and defect density by calculating the intensity ratio of D band and G band ($I_D/I_G$), where $I_D$ and $I_G$ represented the integrated intensities [99,100].

![Example Raman spectrum of a thermally reduced graphene (by Hummer’s Method).](image)

Figure 3. Example Raman spectrum of a thermally reduced graphene (by Hummer’s Method).

Even though studies on Si/Graphene composites routinely utilize SEM, TEM, AFM and Raman, no systematic study has been found that investigates how the quality of Graphene impacted electrochemical performance. In the study of applications such as bioelectric sensors, optical electronics and photovoltaic cells, it was discovered that single or mono layer, large, flat and defect-free structures were desirable for these applications [51,101,102]. Battery electrodes may demand a different set of qualities.

There are a lot of variables in the Si/Graphene nanocomposites, including variables concerning Si (morphology, synthesis routine, and the dimensions), variables concerning graphene nanosheets (graphene synthesis routine) and variables in assembly methods (direct mixing, chemical bonding, and physical attraction). Due to the large variation in synthesizing conditions and lack of complete information, comparative study to identify desirable properties of graphene for Si/Graphene composite anodes based solely on literature study is not feasible. It is critical to isolate certain properties as was done in some preliminary work by Cen and Liang et al. In this work, Si nanoparticle size and surface treatment were maintained constant while the graphene properties were varied by different graphene oxide reduction conditions [103].

4. Conclusions

When Si is used as the anode, carbon coating is needed. Si/Graphene nanocomposites are considered one of the most promising anode materials for next generation high energy density LIBs. Many studies have explored various synthesis methods to form Si/Graphene composite anodes. Currently, the following challenges remain: (1) high cost of synthesizing nano-sized Si and graphene nanosheets; (2) low electrode loading weight; (3) still unsatisfied cycling performance due to volume
expansion. The quality of carbon materials employed in Si/C composites affects the electrochemical performance of the resulted anode and needs to be understood.

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