Fault Analysis and Design of a Protection System for a Mesh Power System with a Co-Axial HTS Power Cable

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Abstract: The uses of high-temperature superconducting (HTS) cables pose a challenge of power system protection since the impedance of the HTS cable is varied during fault conditions. The protection systems should be designed properly to ensure the reliability and stability of the whole system. This paper presents a fault analysis of the co-axial HTS cable in the mesh system and proposes a coordinated protection system. In the proposed protection system, the main protection is the differential current relay whereas the backup protections are the overcurrent and directional overcurrent relays. The normal and abnormal relay operations are considered to analyze the transient fault current in the HTS cable and evaluate the performance of the proposed coordinated protection system. Characteristics of cable impedances and temperatures under various fault conditions are presented. The proposed protection scheme is validated by the simulation in the PSCAD/EMTDC program. Simulation results show that the coordinated protection scheme could successfully protect the HTS cables in both normal and abnormal relay operations.

Keywords: coordinated protections; high-temperature superconducting (HTS) cable; protective relays

1. Introduction

High-temperature superconductors (HTS) exhibit superconducting behavior at critical temperature that is varied according to the layered materials. Magnesium diboride (MgB$_2$) is generally regarded as a conventional HTS with its critical temperature of 39 K. Cuprate superconductors such as YBCO and BSCCO with transition temperatures above the 77 K boiling point of liquid nitrogen have been found [1]. Iron-based superconductors are considered the second class of HTS with a critical temperature in excess of 100 K [2]. High-temperature superconductors have become suitable for practical uses and have great potential for wide-ranging technological applications, such as HTS cables.

HTS cables are able to conduct bulk power at low voltage cables, which results in the widespread application in electric power systems [3–5]. The uses of HTS cables in HVDC transmission systems have been presented in [6–8]. In urban areas where land availability is limited, the HTS cable can replace the traditional high voltage transmission system to supply power for the urban load [9–11]. Since the applications of HTS cables in power systems have been increased significantly, fault analysis and designing the protection system of HTS cables are essential to ensure the stability and reliability of power systems.

In order for designing a protection system, fault analysis of the HTS cable has been presented in several studies. The fault analysis of a 110 kV HTS power cable presented in [12] showed that the fault current could be beyond the critical current in case of an insulation breakdown fault. The transient
analysis of the DC HTS power cable in the HVDC system was investigated in [13]. Since the converter controller of the HVDC system could play the role of current limiter, the DC HTS cable could operate normally without the quenching phenomenon. The transient current analysis of a 66 kV HTS cable in [14] indicated that the uses of copper shield layers could protect the HTS tape layer from burning out. The real-time simulation of the HTS power cable was carried out in [15] to analyze the fault current characteristic, in which the real miniaturized HTS power cable is incorporated into the 22.9 kV utility power system. Fault analysis has shown that the impedance of the HTS cables could be varied significantly during transient, which causes a negative impact on the protection system.

The impacts of HTS cables on power system protection have been studied in [16]. It was shown that the change of cable impedance during a fault has a negative impact on the distance relay. In addition, the setting of the overcurrent relay in the traditional power system should be revised due to a very high fault current. An improved protective relay system was proposed in [17], in which the decision-making algorithm was proposed to improve the performance of differential current relay. In [18], the real-time protection algorithm of a HTS power cable was proposed. Depending on the cable property and geometry, the zero sequence component of transient current exists when the quench occurs in the HTS cable. Thus, based on the vector analysis of fault current by the symmetrical coordinate method, the real-time protection system is designed to protect the HTS power cable. The action criterion protection was proposed in [19], in which the electrical and non-electrical signals were used to detect the quench fault of the HTS cable. An optimal protection scheme of HTS power cable and fault current limiter was proposed in [20] to deal with the change of system configuration.

Various studies have analyzed the fault current characteristic and proposed the protection system of the HTS power cable in the utility power system. However, existing studies only deal with the uses of HTS cables in the radial feeder system where the protection system could be simply designed by the differential current and overcurrent relays. In case the HTS power cable is used in the mesh system, the bidirectional power flow through the HTS cable leads to the complex design of the protection system. In order to address the problem, this study deals with the application of the HTS cable into the mesh power system. The main contributions of this study are as follows:

- Propose coordinated protection of the HTS cables in the mesh power system. In the proposed coordination protection of the HTS cables, the main protection is the differential current relay while the backup protections are the overcurrent (OCR) and directional overcurrent relays (DOCRs). Coordination between OCR and DOCR is proposed to protect the HTS cables.
- Analyze the transient characteristic of HTS cables under normal and abnormal relay operations. The transient current, cable resistances, and temperatures under fault conditions are evaluated.

The rest of this paper is organized as follows. Section 2 presents the modeling of the HTS power cable in the mesh system. The proposed coordinated protection scheme is described in Section 3. Simulation results are shown in Section 4 and the conclusion of this paper is provided in Section 5.

2. System Description

In the case of urban areas where land availability is limited, it is important to establish a bulk capacity underground cable with high reliability. Since the conventional distribution line cable has the limitation of increasing transfer capability, multi-circuit underground cables along with the construction of new substations are essential. However, this solution has a negative impact on the environment and capital cost. With the capability of conducting bulk power transfer at low voltage and low losses, the HTS cable offers an alternative solution to enlarge the transfer capacity of the transmission and distribution networks and reduce the substation facilities in a metropolitan city.

The use of HTS cables causes adverse effects on the coordination protection of the power system due to the variable impedance during a short-circuit, especially in the case of a mesh system. Figure 1 shows a typical mesh system with the use of HTS cables. Two 22.9 kV sources supply powers for local load through two HTS cables. The nominal voltage of the HTS cable is 22.9 kV and its capacity
is 60 MVA. It is assumed that the length of each HTS cable is 1 km. The co-axial HTS cable using Cu-formers is used to conduct powers. Since this study is a continuation of the research presented in [21], the simplified HTS cable model for the transient analysis in previous work is adopted in this study for fault analysis and design of the protection system. Figure 2 shows the operation characteristic and the equivalent scheme of the co-axial HTS cable. The resistance/temperature characteristic of the co-axial HTS cable model is developed in the PSCAD/EMTDC program by the custom model with FORTRAN source code. The variable RLC components are used to model the resistances of the HTS cable. A custom model with a transformer section is used to represent the self and mutual inductances of the HTS cable. More explanation of the co-axial HTS cable could be found in [21]. Detailed parameters of the tested system are given in Table 1.

Figure 1. Tested system and configuration of co-axial high-temperature superconducting (HTS) cable.

Figure 2. Operation characteristic and equivalent scheme of co-axial HTS cable adopted from [21]: (a) current flow into each phase in the normal state; (b) current flow into each phase in the transient state; (c) equivalent circuit of the HTS power cable.
3. Design of Coordinated Protection

Typically, power system components are protected by the main and backup protection. The main protection provides fast and selective clearing of any circuit fault whereas the backup protection is used to isolate the faulty sections in case the main protection fails to function properly. The main and backup protections should be coordinated with sufficient time delay to ensure that the main protection trips first then the backup protection will trip. Figure 3 shows the protection of the HTS cable in the mesh system. The differential current relays (DCR, 87) are used as the main protection of the HTS cables and the overcurrent (OCR, 50/51) and directional overcurrent (DOCR, 67) relays are used for backup protection of HTS cables. The modeling of protective relays in the PSCAD/EMTDC program is based on the master library of the protection section.

![Figure 3. Coordinated protection of the tested system.](image)

3.1. Procedure for Designing Coordinated Protection

The procedure for designing the coordinated protection system is shown in Figure 4. Firstly, the maximum and minimum fault currents are calculated. Since the impedance of the HTS cable is varied during the fault condition, the fault current is calculated from the detailed HTS cable model in the PSCAD/EMTDC program. Based on the maximum fault current, the parameters of the current transformers are selected accordingly. Then the pickup current of relay and its setting parameter are calculated. The setting parameters of relays are based on the design of coordinated protection of the HTS cable, in which the overcurrent relays play the roles of backup protection for both HTS cables in the tested system.

![Figure 4. Process of designing protective relays.](image)

### Table 1. System parameters.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nominal frequency</td>
<td>( f )</td>
<td>60 Hz</td>
</tr>
<tr>
<td>Cable length</td>
<td>( L )</td>
<td>1 km</td>
</tr>
<tr>
<td>Critical current</td>
<td>( I_c )</td>
<td>5 kA</td>
</tr>
<tr>
<td>Resistance of HTS layer at normal state</td>
<td>( R_{HTS,\text{norm}} )</td>
<td>0.00000063 ( \Omega )/m</td>
</tr>
<tr>
<td>Resistance of HTS layer at transient state</td>
<td>( R_{HTS,\text{norm}} )</td>
<td>2.5 ( \Omega )/m</td>
</tr>
</tbody>
</table>
3.2. Design of Differential Current Protection

The main protective relays of HTS cables and transformers are DCR and its characteristic curve is shown in Figure 5. The dual-slope current differential relay module from the master library of the PSCAD/EMTDC program is used in this study. The formulas of differential current threshold \( i_{diff} \) and bias current threshold \( i_{bias} \) are given by the following forms:

\[
\begin{align*}
    i_{diff} &= |i_1 + i_2| \\
    i_{bias} &= \frac{|i_1| + |i_2|}{2}
\end{align*}
\]

where \( i_1 \) and \( i_2 \) are the currents from two sides of the HTS cables or transformers.

![Figure 5. Characteristic curve of directional overcurrent relays (DCR).](image)

Figure 6 shows the implementation of the DCR model in the PSCAD/EMTDC program. The measured currents from CTs are analyzed by Fast Fourier Transformation to obtain the magnitude and phase of the fundamental fault currents. They are then compared to detect the fault in the HTS cables.

![Figure 6. Implementation of DCR in PSCAD/EMTDC.](image)

3.3. Design of Time-Graded OCRs

OCRs are used as backup protection for HTS cables. Since two HTS cables are used to conduct powers from two sources, the directional overcurrent relays (DOCRs) are required to detect the current flow in a particular direction in the HTS cables. The DOCRs consist of two elements: a directional element and an OCR. Coordination among OCR and DOCR should be considered to discriminate between faults. In the proposed protection system in Figure 3, relay 1 should coordinate with relay 3...
and relay 2 should coordinate with relay 4. The general rule of coordinating two overcurrent relays is to apply the coordination time interval (CTI) in Equation (3) at the short-circuit current or maximum load current.

\[
CTI = t_{\text{trip}} + \Delta t
\]  

(3)

where \( t_{\text{trip}} \) is the relay trip time and \( \Delta t \) is the grading time.

In this study, in order to improve the reliability of the protection system, the definite time overcurrent and inverse time overcurrent relays are used in OCRs, in which the pickup current of definite time overcurrent relay is designed based on the short-circuit current and that of inverse time overcurrent relay is selected by 150% of maximum load current. The short-circuit current is calculated by a detailed simulation of the HTS cable system in the PSCAD/EMTDC program. Three-phase-to-ground-fault is applied to the HTS cable system at different locations. Regarding each fault location, the maximum magnitude of the short-circuit current is recorded at different buses, which will be used to calculate the relay setting. The tripping characteristic of inverse time overcurrent relay is given by Equation (4).

\[
t_{\text{trip}} = TD\left(\frac{A}{M^p - 1} + B\right)
\]  

(4)

where \( TD \) is the time dial; \( A, B, \) and \( p \) are the trip characteristic constants given in Table 2, which define the OCR characteristics; \( M = I/I_s \) in which \( I \) is the measured current; and \( I_s \) is the relay setting current.

**Table 2. Trip characteristic constants.**

<table>
<thead>
<tr>
<th>Constant</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>( A )</td>
<td>39.85</td>
</tr>
<tr>
<td>( B )</td>
<td>1.084</td>
</tr>
<tr>
<td>( p )</td>
<td>1.95</td>
</tr>
</tbody>
</table>

The design of a coordinated protection system starts with an initial calculation of DOCR (CB3) with the actual tripping time of 0.2 s since it is the backup element of DCR, as shown in Figure 7. The full load current is calculated by

\[
I_L = \frac{P}{\sqrt{3}V} = \frac{60 \times 10^6}{\sqrt{3} \times 22.9 \times 10^3} = 1512.7 \ A
\]  

(5)

Based on the full load current, the ratio of the current transformer is selected as 2000/5 and the pickup current of DOCR is 10 A. The TD value of OCR is calculated as

\[
TD = \frac{t_{\text{trip}}}{A} + B = \frac{0.2}{\frac{39.85}{1.5 \times 1512.7 + \frac{1.95}{22.9 \times 10^3}} + 1.084} = 0.0014
\]  

(6)

The graded time of DOCR (CB3) and OCR (CB1) is 0.5 s, resulting in the actual tripping time \( t_{\text{trip}} \) of OCR being equal to 0.7 s. Thus, the TD value of OCR is calculated as

\[
TD = \frac{t_{\text{trip}}}{A} + B = \frac{0.7}{\frac{39.85}{1.5 \times 1512.7 + \frac{1.95}{22.9 \times 10^3}} + 1.084} = 0.0049
\]  

(7)

The coordinated characteristic of OCR and DOCR of the tested system is shown in Figure 8, which includes the characteristics of definite time overcurrent and inverse time overcurrent relays.
4. Simulation Results

The internal three-phase short-circuit fault in the HTS cable was applied to validate the performance of the proposed protection system. It is assumed that the fault is applied at 0.5 s and the fault resistance is calculated by Warrington’s equation in [22], as given by Equation (8). The mechanism delay of the medium voltage circuit breaker (CB) is five cycles for a complete opening time.

\[ R_f = \frac{28707.35 \times L}{I A} \]  

where \( R_f \) is the arc resistance, \( L \) is the arc length, and \( I \) is the RMS value of fault current.

4.1. Case 1: Normal Operation

The tested system shown in Figure 9 is used to verify the proposed coordinated protection system in case of normal relay operation. The internal fault of the HTS2 occurs at 0.5 s. The relay tripping signals of two HTS cables are shown in Figure 10, in which the rising signal indicates that the relay detects the fault and the tripping signal is generated. It can be seen that the fault is detected by the DCR of the HTS2 at 0.508 s. The trip signal from DCR is sent to the CB3 and CB4 to isolate the fault. Figure 11 shows the operation status of circuit breakers, in which the rising signal means the circuit breaker is opened to isolate the fault. It is shown that two CBs are opened completely at 0.592 s. The CB opening time is longer than the detection time due to the CB’s mechanism delay.

Figure 7. Grading scheme of the tested system.

Figure 8. Protection coordination for the HTS cable system.

Figure 9. Tested system.
The tested system shown in Figure 9 is used to verify the proposed coordinated protection system in case of normal relay operation. The internal fault of the HTS 2 occurs at 0.5 s. The relay detects the fault and the tripping signal is generated. It can be seen that the fault is detected by the DCR of the HTS2 at 0.508 s. The trip signal from DCR is sent to the CB 3 and CB4 to isolate the fault. Figure 10 shows the operation status of circuit breakers, in which the rising signal means the circuit breaker is opened to isolate the fault. It is shown that two CBs are opened completely at 0.592 s. The circumference of the fault is cleared. The CB opening time is longer than the detection time due to the CB’s mechanism delay.

The three-phase transient currents through two HTS cables are shown in Figure 12. Initially, two HTS cables conduct an RMS current of 2 kA to supply power for the local load. The fault at 0.5 s causes a rapid increase of HTS2 current from 2 kA to 24.5 kA. The current through the HTS 2 is decreased to zero after 0.592 s due to the opening of CB3 and CB4. Meanwhile, the current in HTS 2 increases after clearing the fault. Figure 13 shows the power flow into two HTS cables. Initially, two cables conduct the same amount of power. Three-phase-to-ground fault occurs at 0.5 s, resulting in a rapid increase of powers through both HTS cables from 60 MW to 510 MW. After clearing the fault at 0.592 s, the power in HTS 2 is reduced to zero while the power in HTS 1 is doubly increased compared to the initial condition.

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The HTS cable resistances are shown in Figure 14, which illustrates that the cable resistances are increased when the fault occurs. Due to the geometric structure of the HTS cable, the increased resistance of former phase A is smallest whereas it is highest in case of former phase C. The temperatures of the HTS cables shown in Figure 15 indicate that the temperature of former phase C is the highest (~76 K) compared to other phases.

It can be observed that the main protection of the mesh system is operated correctly. Although the current of HTS1 increases significantly, the main and backup protection systems of HTS1 (OCR and DOCR) do not operate in this case since the DCR of HTS2 detected the fault and isolated the HTS2 from the power system.
4.2. Case 2: Failure of DCR

It is assumed that the DCR of HTS2 fails to detect the internal fault of HTS2. The tested system shown in Figure 16 is used to evaluate the performance of backup protection. The tripping signals of relays are shown in Figure 17, in which the rising signal shows the fault identification of relays. It can be seen that the OCR and DOCR are operated to protect the power system from the fault. The fault occurs at 0.5 s and it is detected by DOCR at 0.76 s and OCR at 0.86 s. The rising signal in Figure 18 shows the opening status of CBs. CB3 and CB4 received the tripping signals from OCR and DOCR; after a five-cycle delay of the CB mechanism, two CBs are opened completely to protect the HTS cable.

![Tested system in case of DCR failure.](image)

**Figure 16.** Tested system in case of DCR failure.

![Relay trip signals in case DCR fails: (a) relays in HTS1; (b) relays in HTS2.](image)

**Figure 17.** Relay trip signals in case DCR fails: (a) relays in HTS1; (b) relays in HTS2.

![Operation of circuit breakers in case DCR fails.](image)

**Figure 18.** Operation of circuit breakers in case DCR fails.

Three-phase currents of two HTS cables are shown in Figure 19 and power flow into two cables is shown in Figure 20. Three-phase-to-ground fault occurred at 0.5 s and lead to a rapid increase of current and power through two HTS cables. The opening of CB3 at 0.84 s results in the reduction of current and power in HTS1 and the increase of current and power in HTS2. The fault is completely isolated at 0.94 s by opening CB4, which is indicated by the zero current and power of HTS2.

The resistances and temperatures of two HTS cables are shown in Figures 21 and 22. It can be seen that the highest temperature rise is found at the former phase C of HTS2, which is about 102 K. It is observed that the backup protection of HTS2 is operated successfully to protect the power system from the internal fault at the HTS2.
The proposed coordinated protection scheme, the internal fault is detected by two OCRs at 0.86 s. The rising signal in Figure 25 describes the CB opening status. The internal fault is completely isolated by opening CB1 and CB4 at 0.94 s.

However, the main and backup protective relays (DCR and DOCR) fail to detect the fault. By using HTS1 and HTS2, the tested system, in this case, is shown in Figure 23. The operation of relays is shown in Figure 24, in which the rising signal represents the fault detection by relays. The fault occurs at 0.5 s, and the current and power in HTS 1 and the increase of current and power in HTS 2. The fault is completely isolated at 0.94 s by opening CB4, which is indicated by the zero current and power of HTS2.

It is observed that the backup protection of HTS2 is operated successfully to protect the power system from the internal fault at the HTS2.

Finally, the failures of DCR and DOCR are considered to evaluate the coordinated protection of HTS cables in case DCR fails. (a) HTS1; (b) HTS2. The resistances and temperatures of two HTS cables are shown in Figures 21 and 22. It can be seen that the highest temperature rise is found at the former phase C of HTS2, which is about 102 K.

Figure 19. Currents in HTS cables in case DCR fails: (a) HTS1; (b) HTS2.
Figure 20. Conducted power in HTS cables in case DCR fails.
Figure 21. HTS cable resistances in case DCR fails: (a) HTS1; (b) HTS2.
Figure 22. HTS cable temperatures in case DCR fails: (a) HTS1; (b) HTS2.
4.3. Case 3: Failures of DCR and DOCR

Finally, the failures of DCR and DOCR are considered to evaluate the coordinated protection of HTS1 and HTS2. The tested system, in this case, is shown in Figure 23. The operation of relays is shown in Figure 24, in which the rising signal represents the fault detection by relays. The fault occurs at 0.5 s, however, the main and backup protective relays (DCR and DOCR) fail to detect the fault. By using the proposed coordinated protection scheme, the internal fault is detected by two OCRs at 0.86 s. The rising signal in Figure 25 describes the CB opening status. The internal fault is completely isolated by opening CB1 and CB4 at 0.94 s.

![Figure 23. Tested system in case DCR and DOCR fail.](image1)

![Figure 24. Relay trip signals in case DCR and DOCR fail: (a) relays in HTS1; (b) relays in HTS2.](image2)

![Figure 25. Operation of circuit breakers in case DCR and DOCR fail.](image3)

Three-phase currents of two HTS cables are illustrated in Figure 26 and the power flows through two HTS cables are shown in Figure 27. The three-phase-to-ground fault at 0.5 s causes the rapid increase of current and power, in which the peak fault current is 27 kA and peak power is 520 MW. The fault is completely isolated by opening CB1 and CB4 at 0.94 s, resulting in the reduction of current and power of the two HTS cables to zero. The cable resistances are shown in Figure 28 and the cable temperatures are shown in Figure 29. The highest temperature rise is in the former phase C, which is about 98 K. It can be seen that the coordinated protection of two HTS cables is operated successfully to protect the HTS cable system from the fault.
Figure 26. Currents in HTS cables in case DCR and DOCR fail: (a) HTS1; (b) HTS2.

Figure 27. Conducted power in HTS cables in case DCR and DOCR fail.

Figure 28. HTS cable resistances in case DCR and DOCR fail: (a) HTS1; (b) HTS2.
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