Thermal Analysis of Power Rectifiers in Steady-State Conditions

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Abstract: Power rectifiers from electrical traction systems, but not only, can be irreversibly damaged if the temperature of the semiconductor junction reaches high values to determine thermal runaway and melting. The paper proposes a mathematical model to calculate the junction and the case temperature in power diodes used in bridge rectifiers, which supplies an inductive-resistive load. The new thermal model may be used to investigate the thermal behavior of the power diodes in steady-state regime for various values of the tightening torque, direct current through the diode, airflow speed and load parameters (resistance and inductance). The obtained computed values were compared with 3D thermal simulation results and experimental tests. The calculated values are aligned with the simulation results and experimental data.

Keywords: power rectifier; temperature distribution; mathematical model; thermal modeling and simulation

1. Introduction

A great challenge for power electronic systems is to remove the heat from the power devices in an efficient and cost-effective manner. Due to the needs of high power density and to the miniaturization of power converters, the components have to operate next to their thermal limits [1]. One of the main concerns in many power converters design procedure is to controlling the heat as the producer’s moves toward all-surface-mount implementations. Thus, in the early design phase of the power converters it is very important to understand the thermal aspects of the components in order to improve the devices thermal performances [2]. The available thermal models have their boundaries to correctly estimate the thermal behavior in the IGBTs: The usually used thermal models based on one-dimensional RC node have limits to estimate the temperature variation inside devices. A new lumped 3D thermal model is suggested in [3], which can be simply described from FEM simulations and may achieve the critical thermal distribution in long-term evaluations. The boundary constraints for the thermal investigations are considered, which may be adjusted to various real-field applications of power electronic converters. In the existing losses and thermal models, just the electrical loadings for design variables are considered. In [4], a full losses and thermal model taking into consideration the device rating as input variables is also presented. A mathematical relation between the power loss, thermal impedance and the silicon area for the IGBT is estimated. Thus, all the elements with impact over the loss and thermal aspects are considered with results in an optimal design of the power converter.

For the IGBT modules three usual thermal modeling methods regarding their influence on lifetime estimation are compared in [5]. It is shown that these models have important differences in estimating the cross-coupling terms between the chips in the module.
In [6], for a 1200 A, 3.3 kV IGBT module the thermal analysis is considered by the three-dimensional transmission line matrix method. The results show a three-dimensional visualization of self-heating in the module. The temperature evolution is determined during the PWM load cycles, with a good result regarding the thermal analysis and design. A mathematical modeling for time-variant cooling systems is considered in [7], switching between such networks matched to different edge conditions. The accuracy, in terms of temperature and predicted IGBT lifetime, for Foster and Cauer networks is analyzed and compared to errors in the thermal-interface-material resistance. A power electronic converter structure based on power losses and thermal models are considered in [8] for a three level active neutral point clamped voltage source converter using press-pack IGBT-diode pairs. The thermal model is based on water-cooled press-pack switches and is simulated for a 6 MW wind turbine grid interface.

The thermal management is an important aspect for the power converters used for vehicles. For the IGBT package modules employed in hybrid electric vehicle applications it is presented in [9] a method to obtain precise models for rapid electrothermal simulations with advantages in reducing the design cycle. For the traction motors, the volume of the motors can be decreased significantly by improving the thermal flows across the inductor. The silicone resin potted high power inductor for DC/DC switch-mode boost converters can be used [10] in comparison with classical air-cooled inductors, simulations and test being presented for a 40 kW DC–DC converter. In [11] is presented an experimental analysis of multichip IGBT package modules, combining rapid transient short-circuit electrical measurements with infrared thermal mapping considering realistic operating conditions. An electrothermal concise model of the multichip system is described, considering all the important operating and particular electrothermal effects, with application in the railway industry. An electrothermal mathematical model for a real-time thermal simulation of an IGBT in an inverter power module used on hybrid vehicle is considered with estimation of the temperature and power losses [12]. Thermal simulations were realized with high precision for a long real-time, which means more than 10 min, which is important for the vehicles. The peak torque for a traction motor is limited by switching device temperatures. For an 11 kW PMSM motor, it is presented in [13] a drive control strategy that merge the active thermal management model with dynamic DC-link voltage adaptation. Thus, the switching losses can be decreased at low speed by decreasing the bus voltage resulting a considerable inverter losses decrease at low speed.

Power converters for wind turbines are highly used due to the green energy evolution. Wind turbines are using power electronic converters with multichip paralleled IGBT modules. In [14], junction temperatures of chips for various positions with a better thermal coupling impedance model are estimated, and the outcomes are compared with the results of other thermal models.

A two-level power electronic converter for wind turbine is analyzed considering the losses and the junction temperature for the power devices in the case of a large wind speed variation, resulting in the high effect of the junction temperature over the operating point of the induction generator of the turbine [15,16].

Thermal characteristics of various power switching devices are considered in [17] for their impact on the thermal cycling of a 10 MW wind power converter in different working conditions considering IGBT modules and press-pack, the thermal characteristics of the power electronic converter being significantly changed according to the power semiconductor devices technology and their configurations.

In [18] is presented also an electrothermal model of an inverter but implemented in PLECS being used to estimate the IGBT junction temperature with a mission profile for a wind power application. The thermal network is determined from the heating curves of IGBT junction and case temperature that are measured on a power-cycling rig. Junction temperature is estimated with the power dissipation and thermal network. For rapid and precise thermal simulations of power semiconductor modules it is developed a Fourier-based solution, which can estimate the temperature versus time to resolve the heat equation in two dimensions [19], resulting a fast simulation compared to the finite-element (FEM).
Microchannel coolers for thermomechanical performance of power electronic modules with IGBT are analyzed in [20] by using finite element analysis with results in increasing the lifetime modules. Power loss model is used [21] to design a 3 kVA DC/AC high output power density converter considering a connection between the circuit stray parameters and the power losses into the converter. A thermal model of a heatsink as a RC thermal equivalent network that can be embedded in any circuit simulator, it is presented in [22]. The model considers thermal time constants of the heat sink, the convection cooling, thermal hotspots on the heatsink base plate, and thermal coupling between power semiconductor modules mounted onto the heatsink.

Design procedure considerations based on various temperature distribution study cases are realized on a prototype, a 2 kW integrated power electronic semiconductor modules using Cool MOS and SiC diode [23]. The thermal model is elaborated and the power dissipation of every power semiconductors in the active IPEM is calculated through the measurement-based power loss characteristics in datasheet. With the requirements of equal temperature distribution and light thermal interaction between power dice, the modification of temperature gradient distribution with the heat transfer coefficient of heatsink and die position is analyzed. In the [24] literature review for the design and study of thermal via in PCBs for thermal control of power electronic semiconductors lead to a selection for four different models for single power devices. The experimental results match the theoretical anticipation to identify the most effective thermal model via pattern.

For the estimation of the power module temperature field contours at various temperatures, a simplified 3D model of the power module was taken into consideration [25], and steady-state thermal analysis was done. The layout of the power electronic converter is optimized to decrease the heat distribution corresponding to the analysis outcome. The simulations show that an optimal distribution of the power supply module can increase the reliability of the switching mode DC/DC converters. The three-level active-neutral-point-clamped voltage source converter is used to defeat the uneven loss distribution [26]. The junction temperature and the load current of a power electronic converter system are obtained to calculate the power losses. The thermal resistance and the thermal capacitance of different components are used to express the thermal network, which is used to estimate the power device junction temperature, and to analyze transient thermal distribution.

In [27], it proposes the certainty design to the power electronic converter’s conventional compensation controller design with a new concept of a universal dual-loop controller, which uses temperature control loop as well as electric power control loop. The idea is based on a digital implementation of a variable load of power inverter system with real-time measurement approach of the chip’s surface temperature. The novelty is to get a better thermal control method of carrier frequency adjustment through experimental implementation during the full life cycle of the power electronic converter. A back-to-back 2-level/3-level inverter has been designed and developed such that the power will flow in both directions while the converter will perform better under arbitrary load conditions analyzing the thermal dissipation of the semiconductors [28]. The thermal investigation has been performed in the case of an IGBT module with a specifically integrated real time current controller. In [29] it is proposed to use the active thermal management to reduce the switching losses during transient regime in order to assure a high current without overcoming the temperature limits, reducing the overdesign of power converters.

An electrothermal design methodology is proposing and a reliability study is realized [30] for converters used for photovoltaic application, which is the distributed maximum power point tracking converter. In [31] is presented an electrothermal analysis based on the reduced order modeling technique in which the zero-dimensional thermal network is obtained from the three-dimensional IGBT semiconductor module packaging structure and incorporated with the electrothermal model of the chips.

Considering the previous works of different research teams, it can be outlined that the existing thermal models have their limits to predict the thermal behavior of different types of power semiconductor devices, especially in the case of IGBTs. It has been proposed simplified 3D thermal
models based on FEM and also the well-known Foster and Cauer networks to analyze inverters’ behavior from thermal point of view. More, the researchers developed different methods to monitor the junction temperature of the IGBTs with the aim to check their reliability.

The aim of this paper is to investigate the thermal behavior of the power diodes from a rectifier bridge used in electrical traction during steady-state conditions.

The paper is arranged as follows, in Section 2 the mathematical model to calculate the junction and case temperature will be developed. Then, in Section 3, the calculated values will be compared with the simulated results of a 3D thermal model of a power assembly, which includes two power diodes mounted on the heatsink. Finally, the proposed mathematical model will be validated through a series of experimental tests, in Section 4 followed by the conclusion section, where the most important outcomes of the paper are highlighted.

2. Mathematical Model

The goal of the thermal analysis is to establish a mathematical model that will be used to compute the junction and the case temperature for a power diode, which is the main component from a power traction rectifier. The power loss of the diode during steady-state conditions is a sum of the following terms:

\[ P = P_F + P_R + P_c \] (1)

where:

- \( P_F \) means the power loss during direct conduction;
- \( P_R \) is the power loss in the case of locked conditions;
- \( P_c \) is the power loss during commutations.

In the situation of direct conduction, the power losses can be calculated using the following expression,

\[ P_F = V_T I_{TAV} + r_T I_{RMS}^2 \] (2)

and it is the observed that depends both on diode intrinsic characteristics through the parameters \( V_T \) and \( r_T \), and current waveform which flows through the power diode. In the case of a resistive inductive load, the electric current can be described by the next formula,

\[ i(t) = I_m \sin(\omega t - \varphi) + I_m \sin \varphi e^{-\frac{\omega t}{\sqrt{\pi}R}} \] (3)

Thus, it results in the RMS current:

\[ I_{RMS} = \left( \frac{1}{2\pi} \int_0^\pi i^2(t) d(\omega t) \right)^{1/2} = \frac{I_m}{\sqrt{2\pi}} \left[ \frac{\pi}{2} - \frac{2\sin \varphi \omega L}{1 + (\frac{\omega L}{R})^2} \left( \sin \varphi - \frac{\omega L}{R} \cos \varphi \right) \left( e^{-\frac{\pi \omega L}{2R}} + 1 \right) + \frac{2\omega L}{R} \sin^2 \varphi \left( e^{-\frac{\pi \omega L}{2R}} - 1 \right) \right]^{1/2} \] (4)

Additionally, the average current has the expression,

\[ I_{FAV} = \frac{1}{2\pi} \int_0^\pi i(t) d(\omega t) = I_m \frac{1}{\pi \cos \varphi} \] (5)

Therefore, considering the expressions of the RMS Equation (4) and average Equation (5), the relation to calculate the power losses during direct conduction, becomes,

\[ P_F = V_T I_m \frac{1}{\pi \cos \varphi} + r_T I_{RMS}^2 \left[ \frac{\pi}{2} - \frac{2\sin \varphi \omega L}{1 + (\frac{\omega L}{R})^2} \left( \sin \varphi - \frac{\omega L}{R} \cos \varphi \right) \left( e^{-\frac{\pi \omega L}{2R}} + 1 \right) + \frac{2\omega L}{R} \sin^2 \varphi \left( e^{-\frac{\pi \omega L}{2R}} - 1 \right) \right] \] (6)
The power losses in the case of locked conditions $P_R$ can be calculated by the product between the maximum reverse current $I_{RM}$ and the maximum repetitive reverse voltage $V_{RRM}$,

$$P_R = I_{RM}V_{RRM}$$  \(7\)

Both parameters $I_{RM}$ and $V_{RRM}$ depend on the type of the power diode and their values are depicted in the datasheet of the diode. The commutation power loss $P_c$ can be calculated using the following relation,

$$P_c = V_{Rmax}sQ_s = \sqrt{6}U_2sQ_s$$  \(8\)

where the maximum reverse voltage $V_{Rmax}$ depends on the type of the power rectifier topology. It has been considered a B6 bridge rectifier. In this case, the maximum reverse voltage is equal with the secondary voltage of the supply transformer, multiplied by the coefficient $\sqrt{6}$.

In the case of traction power rectifiers, the diodes are mounted on aluminum heatsinks with the aim to ensure an efficient cooling of the semiconductor junction. Thus, the main components of the current path within the power assembly of the bridge rectifier, includes power diode, heatsink and busbar. Therefore, in addition to the power loss of the diode $P$, there are also the power losses of the contact resistance between the case diode and the heatsink as main component of the current path. The power loss because of the contact resistance can be computed with the known formula,

$$P_{contact} = R_c^2$$  \(9\)

Further, it has been examined the case in which the rectifier bridge is composed from power diodes type SKN 300 threaded stud M16 × 1.5 mm. There is an inverse proportional relationship between the contact resistance $R_c$ and the contact force $F$, which will be calculated knowing the tightening torque $M$, the screw diameter $d$, the thread pitch $p$ and the friction coefficient $\mu$, with the following formula [32],

$$F = \frac{2d}{M} \left[ \cos\left(\arctg \frac{p}{d}\right) - \mu \sin\left(\arctg \frac{\mu}{d}\right) \right]$$  \(10\)

The nonlinear variation between contact resistance $R_c$ and contact force $F$ can be represented by a mathematical function. Starting from an experimental dataset, the curve of the contact resistance has been fitted to the following expression,

$$R_c = \frac{1}{a_1 + b_1 F + c_1 F^2}$$  \(11\)

The coefficients of the fitting curve have the next values: $a_1 = 397.53$, $b_1 = 0.98625$ and $c_1 = 0.00011$. The comparison between experimental and fitting curve of the contact resistance variation vs. contact force is presented in the Figure 1.

Thus, using the expressions (4), (10) and (11), the contact power losses can be computed as follows,

$$P_{contact} = \frac{\mu^2}{a_1 + b_1 F + c_1 F^2} \left[ \frac{d}{2} - \frac{2 \sin \phi \mu}{\sqrt{1 + \left(\frac{d}{2}\right)^2}} \left( \sin \phi - \frac{\mu}{\sqrt{d}} \cos \phi \right) \left( e^{-\frac{\mu}{\sqrt{d}} \pi} + 1 \right) - \frac{\mu}{\sqrt{d}} \sin^2 \phi \left( e^{-\frac{\mu}{\sqrt{d}} \pi} - 1 \right) \right]$$  \(12\)

Finally, the total power loss considering both diode power loss and contact power loss has the expression:

$$P_{tot} = P + P_{contact} = P_F + P_R + P_c + P_{contact}$$  \(13\)
After the previous expressions (6), (7), (8) and (12) have been replaced in the above relation, we obtained the diode total power dissipated as:

\[
P_{tot} = \frac{1}{\pi \cos \phi} \left[ \frac{\pi}{2} + \frac{2 \sin \phi \sin \frac{v}{\sqrt{2}}}{1 + \left( \frac{v}{\sqrt{2}} \right)^2} \left( \sin \phi - \frac{v}{\sqrt{2}} \cos \phi \right) \left( e^{\frac{v}{\sqrt{2} R}} - 1 \right) - \frac{2}{\sqrt{2}} \sin^2 \phi \left( e^{2 \frac{v}{\sqrt{2} R}} - 1 \right) \right] +
\]

\[
I_{RM} V_{RMM} + \sqrt{6} U_2 f Q_a + \frac{I_{RM} V_{RMM}}{4} \left( \sin \phi - \frac{v}{\sqrt{2}} \cos \phi \right) \left( e^{\frac{v}{\sqrt{2} R}} - 1 \right)
\]

The above expression regarding the calculation of the total power dissipated at the power diode will be used to determine the junction and case temperature of the analyzed power diode. The following calculation formulas can be used to get the values for the junction temperature,

\[
\theta_j = \theta_a + (R_{thjc} + R_{thck} + R_{thka}) P_{tot}
\]

and for the case temperature of the power diode,

\[
\theta_c = \theta_a + (R_{thck} + R_{thka}) P_{tot}
\]

The values of the junction-case and case-heatsink thermal resistance can be obtained from the power diode datasheets. Thus, in the case of SKN 300 power diode, the following values were identified: \( R_{thjc} = 0.15 \ degreeC/W \) and \( R_{thck} = 0.02 \ degreeC/W \). Generally, the heatsink-environment thermal junction was not a constant value and it depended on the cooling method (forced or natural) and the heatsink type. The heatsink datasheets present variation curves of the heatsink-environment thermal junction vs. air speed. The cooling of the SKN 300 power diode can be provided by aluminum heatsink type R150-E50 manufactured by IPRS Baneasa. From the datasheet, it can be obtained the nonlinear variation of heatsink-environment thermal resistance vs. air speed. This curve can be approximated with the following mathematical expression:

\[
R_{thka} = a_2 + b_2 \sigma_{air}^2 + c_2 \sigma_{air}^2 \ln \sigma_{air} + d_2 \frac{\ln \sigma_{air}}{\sigma_{air}^2}
\]

where the parameters of the fitting curve are: \( a_2 = 0.25035 \), \( b_2 = -0.015359 \), \( c_2 = 0.006483 \) and \( d_2 = -0.189932 \). The normal variation of decreased heatsink-environment thermal resistance for increasing air speed can be observed in Figure 2.
So, the junction and case temperature of the power diode will be calculated using the next expressions:

\[
\theta_j = \theta_a + \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) P_{tot}
\]

(18)

and,

\[
\theta_c = \theta_a + \left( R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) P_{tot}
\]

(19)

By replacing in the above formulas, the expression of the total power loss, the calculation relations for the junction and case temperature are finally obtained:

\[
\theta_j = \theta_a + \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) V_T \ln \frac{1 - \cos \varphi}{\pi} +
\]

\[
+ \frac{\xi}{2 \pi} \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) \sqrt{f Q_{U1}} \ln \frac{1 - \cos \varphi}{\pi} \cos \varphi \left( e^{-\frac{2 \pi}{\varphi}} - 1 \right) +
\]

\[
+ \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) \sqrt{f Q_{U2}} \ln \frac{1 - \cos \varphi}{\pi} \sin \varphi \left( e^{-\frac{2 \pi}{\varphi}} - 1 \right) +
\]

and,

\[
\theta_c = \theta_a + \left( R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) V_T \ln \frac{1 - \cos \varphi}{\pi} +
\]

\[
+ \frac{\xi}{2 \pi} \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) \sqrt{f Q_{U1}} \ln \frac{1 - \cos \varphi}{\pi} \cos \varphi \left( e^{-\frac{2 \pi}{\varphi}} - 1 \right) +
\]

\[
+ \left( R_{thjc} + R_{thck} + a_2 + b_2 v_{air}^2 + c_2 v_{air}^2 \ln v_{air} + d_2 \frac{\ln v_{air}}{v_{air}} \right) \sqrt{f Q_{U2}} \ln \frac{1 - \cos \varphi}{\pi} \sin \varphi \left( e^{-\frac{2 \pi}{\varphi}} - 1 \right) +
\]

(21)
The above expressions outline that both case and junction temperature depends on conduction current, tightening torque, load resistance, load inductance and air speed.

3. Three-Dimensional Thermal Model

The thermal analysis has been focused on a power assembly made from two stud rectifier diodes type SKT 340 mounted on the same aluminum heatsink. Actually, the power assembly can be considered as one of the three legs from the three-phase full-bridge rectifier, which provides a DC current on a resistive-inductive load. The datasheet of the rectifier diode outlines the following values for the main parameters of the diode: $V_T = 0.8$ V, $r_T = 0.6$ mΩ, $I_{RM} = 60$ mA, $V_{RRM} = 1200$ V and $Q_s = 200$ µC. Considering the direct current through the power diode with the RMS value of 250 A, it can be calculated the power loss within the diode, which had the value of 119.29 W. In the case of the recommended tightening torque of 30 Nm, the contact resistance had the value of 0.45 mΩ. Thus, for the same value of the direct current flowing through the diode, the power loss because of the contact resistance between heatsink and the diode was about 28.125 W.

The structure of the power diode was highlighted by a solid copper hexagon with a threaded stud, which is used to bolt the component onto a heatsink and discharge the heat produced by the chip. A glass or ceramic cap with a bushing for the cathode terminal provides hermetical case sealing. Case parts are attached by welding or brazing. The chip is straightly soldered onto the disk made of molybdenum to prevent high mechanical pressure in the semiconductor as a result of important differences in the expansion coefficients for silicon and copper. In order to achieve the three-dimensional geometry of the analyzed power assembly, the Pro/Engineering software package has been used. The thermal model included only the components that actively participate in the heat transfer from the power diode to the environment through the heatsink. These components are (Figure 3) the copper braid, molybdenum discs, Silicom chip, solid copper hexagon with threaded stud and aluminum heatsink. Due to the fact that the ceramic housing participates in a very low weight to the heat transfer from the silicon chip to the environment, it was not included in the thermal model.

![Figure 3. 3D geometry of the power diode thermal model: 1—copper braid; 2—molybdenum disc; 3—silicon chip; 4—molybdenum disc; 5—solid copper hexagon with threaded stud and 6—heatsink.](image-url)

The material characteristics of each component part of the power diode and the heatsink are synthesized in the Table 1 and the complete thermal model of the power assembly is shown in Figure 4.
Table 1. Material data of power diode and heatsink.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Copper</th>
<th>Silicon</th>
<th>Molybdenum</th>
<th>Aluminum</th>
</tr>
</thead>
<tbody>
<tr>
<td>( \gamma ) (kg/m(^3))</td>
<td>8900</td>
<td>2330</td>
<td>10220</td>
<td>2700</td>
</tr>
<tr>
<td>( c ) (J/kg(^\circ)C)</td>
<td>387</td>
<td>702</td>
<td>255</td>
<td>900</td>
</tr>
<tr>
<td>( \lambda ) (W/m(^\circ)C)</td>
<td>385</td>
<td>124</td>
<td>138</td>
<td>200</td>
</tr>
</tbody>
</table>

Figure 4. 3D geometry thermal model of the power assembly power diodes heatsink.

On the basis of Pro-MECHANICA software package, some thermal simulations were performed during steady-state conditions. The temperature profile of the power diode, in the case of tightening torque of 30 Nm, air speed value of 1 m/s, direct current of 200 A and the load with a resistance of 20 \( \Omega \) and inductance about 60 mH, is presented in the next pictures, Figures 5 and 6. It can be seen a maximum temperature of the power assembly about 84.94 °C on the silicon chip of the diode and a minimum temperature of 48.34 °C on the heatsink outer surfaces.

Figure 5. Temperature distribution of the power assembly. Cross-section through both power diodes.
Actually, during thermal simulations, the power loss was evenly distributed on silicon chip and contact Energies temperature took into account a concentrated power loss both for power diode and contact resistance. It can be noticed that thermal simulation values were lower than 76.28 to 73.03 °C. At both junction and case temperature variation, the simulated values are placed on the same graph. It can be noticed that thermal simulation values were lower than the calculated ones. This can be explained because the mathematical model of junction and case temperature took into account a concentrated power loss both for power diode and contact resistance.

4. Discussion of the Results

Further on, considering the obtained solutions for the calculation of junction and case temperature of the power diode from the Equation (20), respectively (21), the influence of the direct current, airflow speed and load parameters on thermal behavior of the power diode would be investigated.

The first thermal analysis refers to the variation of junction and case temperature of the power diode against tightening torque at different current values of 200, 250 and 300 A, in the case of airflow speed of 1 m/s, load resistance about 20 Ω and load inductance of 60 mH. It can be seen a decrease in both junction and case temperatures, Figures 7 and 8, when the tightening torques increases. This can be explained because as torque increases, then the contact force between case diode and heatsink increases, which leads to lower values of the contact resistance and contact power loss, and finally, the junction and case temperature values will decrease. For instance, when tightening torque had the value of 15 Nm, the junction temperature was 109.4 °C, Figure 7, and for a torque value of 50 Nm, the junction temperature reached the value of 104.24 °C, so, a difference about 5 °C. For the same variation of the tightening torque, from 15 to 50 Nm, the case temperature decreases from 76.28 to 73.03 °C, Figure 8. At both junction and case temperature variation, the simulated values are placed on the same graph. It can be noticed that thermal simulation values were lower than the calculated ones. This can be explained because the mathematical model of junction and case temperature took into account a concentrated power loss both for power diode and contact resistance. Actually, during thermal simulations, the power loss was evenly distributed on silicon chip and contact.
surface between stud diode and heatsink. This led to an improved heat transfer from the power assembly to the environment, and finally, lower junction and case temperatures compared to those calculated. For instance, in the case of tightening torque of 30 Nm and 300 A, Figure 7, the simulated junction temperature was 105.1 °C, a lower value than 105.95 °C for the calculated temperature and the simulated case temperature was 73.2 °C lower than 74.11 °C, the calculated one, Figure 8.

Figure 7. Junction temperature variation for different current values: 200, 250 and 300 A (v = 1 m/s, R = 20 Ω and L = 60 mH).

Figure 8. Case temperature variation for different current values: 200, 250 and 300 A (v = 1 m/s, R = 20 Ω and L = 60 mH).

A series of experimental tests were performed in order to validate the proposed mathematical model to calculate the junction and case temperature of power diode. A three-phase bridge rectifier with forced cooling supplied through the power transformer Tr it was considered, as can be seen in Figure 9. The main goal was to acquire the case temperatures of the power diode mounted on the aluminum heatsink. The temperature values were acquired with suitable thermocouple Th type K. It used an electronic board AS type AT2 F-16 in order to adapt the small signals provided by
thermocouples to the data acquisition board type PC-LPM-16. This can be programmed by LabVIEW software. The current value can be recorded using the ammeter A.

![Experimental setup](image)

Figure 9. Experimental setup.

From the graph of case temperature variation, Figure 8, it can be noticed that experimental values were higher than the calculated ones. This is explained because the mathematical model considers a uniform cooling of the power diode case. Actually, the airflow did not uniformly cool the surface of the diode case due to the mounting technology of the cooling system for the bridge rectifier. Hence, there was not a good heat transfer from the case to the environment. As an example, in the case of tightening torque of 30 Nm and direct current of 300 A, the experimental case temperature of 75 °C was higher than 74.11 °C, the calculated value.

The second thermal analysis takes into account the junction and case temperature variation against tightening torque for different load resistance values (10, 20 and 100 Ω) when the direct current was 300 A, airflow speed of 1 m/s and load inductance of 10 mH. It can be noticed the decreasing of both junction and case temperature when the torque increased, Figures 10 and 11. The explanation was the same as in previous analyzed case.

![Graph](image)

Figure 10. Junction temperature variation for different load resistance values: 10, 20 and 100 Ω (v = 1 m/s, I = 300 A and L = 10 mH).
In addition, it can be noticed that for higher values of the load resistance, the temperature values decreased. This can be explained because as load resistance increases than the impedance increases and the current decreases, which finally leads to lower power loss and lower junction and case temperatures. For instance, at 30 Nm tightening torque, and 10 Ω load resistance, the junction temperature was 94.67 °C in comparison with the case of 100 Ω load resistance when the junction temperature got the value of 93.11 °C, Figure 10. At the same tightening torque of 30 Nm, the case temperature was 67.01 °C when load resistance was 10 Ω and became 66.03 °C for a load resistance of about 100 Ω, Figure 11. The same as in the previous analyzed case, the simulation values were lower than the calculated junction and case temperatures. As an example, at 15 Nm tightening torque and 10 Ω load resistance, the simulated value was 96.9 °C with respect to 97.72 °C of the calculated junction temperature, Figure 10, and the simulated case temperature was 68.4 °C lower than 68.93 °C, the calculated one. Moreover, on the same graph of case temperature variation, there were placed the experimental data, which were higher than the computed ones, Figure 11. The explanation was the same as in previous case. As an example, at 30 Nm tightening torque and 10 Ω load resistance, the experimental value was 67.4 °C with respect to 67 °C of the calculated case temperature.

The next thermal analysis considered the same variation of junction and case temperature against tightening torque but at different load inductance values from 10 to 160 mH, in the case of a direct current of 300 A, airflow speed of 1 m/s and load resistance of 100 Ω. As in the previous analyzed cases, the temperatures decreased when the tightening torque increased, Figures 12 and 13. For instance, in the case of a load inductance of 10 mH, the junction temperature decreased from 96.14 °C (torque of 15 Nm) to 91.81 °C (torque of 50 Nm), Figure 12, and the case temperature decreased from 67.93 °C (torque of 15 Nm) to 65.09 °C (torque of 50 Nm), Figure 13. Regarding the simulated temperature values, the same trend was confirmed: lower values than those calculated. For instance, in the case of tightening torque of 30 Nm and load inductance of 160 mH, the simulated junction temperature had a lower value of 96.6 °C than 97.08 °C the calculated one, Figure 12 and the simulated case temperature was 67.9 °C lower than 68.53 °C obtained from computations, Figure 13. On the same graph, it can be noticed also the obtained experimental values, which were higher than the calculated ones. For instance, at 30 Nm tightening torque and load inductance of 160 mH, the experimental case value was 68.9 °C with respect to 68.53 °C, the calculated one.

Figure 11. Case temperature variation for different load resistance values: 10, 20 and 100 Ω (v = 1 m/s, I = 300 A and L = 10 mH).
The last thermal analysis investigated the variation of junction and case temperature at different airflow speed values from 2 to 6 m/s when the direct current had the value of 300 A, the load resistance was about 10 Ω and the load inductance was 60 mH. The same decreasing evolution of the temperatures when the tightening torque increased could be observed, both for junction and case temperature, Figures 14 and 15.
As an example, in the case of the air speed value of 2 m/s, at a torque value of 15 Nm there was a junction temperature of 119.07 °C and case temperature of 75.87 °C, and to a torque value of 50 Nm, the junction temperature reached the value of 113.49 °C and the case temperature got a value of 72.72 °C. As in previous analyzed cases, the simulated values of the junction and case temperature were lower than the computed ones: a junction temperature of 114.7 °C in comparison with the calculated value of 115.34 °C in the case of 30 Nm tightening torque, Figure 14, and a case temperature of 72.9 °C lower than 73.36 °C, the calculated value for the same tightening torque, Figure 15. Additionally,
lower than the computed ones: a junction temperature of 114.7 °C in comparison with the calculated value of 115.34 °C in the case of 30 Nm tightening torque, Figure 14, and a case temperature of 72.9 °C lower than 73.36 °C, the calculated value for the same tightening torque, Figure 15. Additionally, on the last graph, it can be noticed the experimental values for the case temperature, which were higher than the calculated ones. For instance, in the case of air speed value of 2 m/s and a torque value of 30 Nm, the experimental obtained value of the case temperature was 74.4 °C with respect to the calculated one of 73.36 °C.

The difference between calculated and experimental values was also due to measurement errors, simplifications in the mathematical model and experimental setup. It could be noticed that the maximum difference between calculated and measured values was less than 2 °C.

5. Conclusions

A new mathematical model to investigate the power diodes’ behavior from a thermal point of view was proposed in this paper. The power diodes belonged to a three-phase bridge rectifier used in electric traction applications. The mathematical model can be used to calculate the junction and case temperature of the power diode for different values of the tightening torque, direct current through the diode, airflow speed and load parameters (resistance and inductance). It outlined the decreasing of both the junction and case temperature when the tightening torque increased. The decrease in temperature was at a maximum of 5 °C while the tightening torque increased more than 3 times from 15 to 50 Nm.

The computed values were compared with the simulation results. Hence, a 3D modeling and thermal simulations of a power assembly including two diodes mounted on the heatsink were performed. The thermal simulation values were lower that the calculated ones because the the mathematical model of junction temperature took into account a concentrated power loss and during thermal simulations, the power loss was evenly distributed on the silicon chip and contact surface, which led to a better heat transfer to the environment. Additionally, the calculated values were compared with some experimental data. It recorded higher experimental values than the calculated ones because the mathematical model considered a uniform cooling of the power diode case and actually, the airflow did not uniformly cool the surface of the diode case, which did not lead to a good heat transfer to the environment.

The proposed thermal model can be used to design different power bridge rectifiers from electric traction but not only this, with an optimized thermal distribution it can also consider the influence of some key parameters both electrical and mechanical. Since the new mathematical model takes into account also some thermal parameters, as thermal resistances related to power diode and heatsink, it can be customized for a wide range of power semiconductor devices mounted on different type of heatsinks from power converters.

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### Nomenclature

- $\theta_j$: means the junction temperature °C
- $\theta_c$: case temperature °C
- $\theta_a$: ambient temperature °C
- $P$: average power loss W
- $r_T$: internal resistance $\Omega$
- $V_T$: voltage drop V
- $R_{thjc}$: thermal resistance between junction and case °C/W
- $R_{thck}$: thermal resistance between case and heatsink °C/W
- $R_{thka}$: thermal resistance between heatsink and environment °C/W
- $I_{FAV}$: average current A
- $I_{RMS}$: root mean square value of the current A
- $I_m$: maximum current A
- $I_{RM}$: maximum reverse current A
- $V_R$: reverse voltage V
- $V_{RRM}$: maximum repetitive reverse voltage V
- $U_2$: secondary voltage V
- $\phi$: phase shift between the current and voltage °el
- $f$: commutation frequency Hz
- $Q_s$: recovery charge C
- $\omega$: pulse current rad/s
- $L$: inductance H
- $R$: resistance $\Omega$
- $v_{air}$: air speed m/s
- $R_c$: contact resistance $\Omega$
- $F$: contact force N
- $M$: tightening torque Nm
- $d$: screw diameter m
- $p$: thread pitch m
- $\mu$: friction coefficient

### References


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