A Programmable 3-Channel Acoustic Wake-Up Interface Enabling Always-On Detection of Underwater Events within 20 µA †

Dinko Oletic, Marko Gazivoda and Vedran Bilas *

Faculty of Electrical Engineering and Computing, University of Zagreb, Unska 3, 10000 Zagreb, Croatia; dinko.oletic@fer.hr (D.O.); marko.gazivoda@fer.hr (M.G.)

* Correspondence: vedran.bilas@fer.hr; Tel.: +385-1-6129-974
† Presented at the Eurosensors 2018 Conference, Graz, Austria, 9–12 September 2018.

Published: 23 November 2018

Abstract: We present an always-on acoustic wake-up sensor interface, designed for prolonging the autonomy of energy-hungry hardware for underwater acoustic surveillance. Proposed design enables the detection of a passing ship by simultaneous listening up to three arbitrarily defined frequency-bands within the 2.5 kHz range, and generates a wake-up signal upon finding a match with a digitally preset template describing signal’s discriminatory time-frequency features. In this paper, we propose the architecture of such fully programmable, multichannel, mixed-signal wake-up circuit. We show the implementation of a PCB prototype, characterize its sensitivity, analyze its current consumption, and verify its response on real-world hydrophone recordings. It is demonstrated that the design consumes only 6.4 µA per channel (in total <20 µA) with ultra-low-power COTS components, while listening.

Keywords: acoustic event detection; underwater surveillance; always-on sensor interface; DSP

1. Introduction

In a variety of underwater passive acoustic surveillance applications (security, marine-biology, environmental science etc.), bandwidth of signals of interest often spans up to 100 kHz. This requires continuous, high-fidelity signal acquisition, at sample rates as high as 100–200 kHz, real-time digital processing, and storage of terabytes of data. State-of-the-art underwater acoustic surveillance equipment typically requires 10–100 mW of power for such tasks [1,2], consequently suffering from either limited autonomy, or requiring bulky energy-storage units (batteries).

In order to miniaturize the passive acoustic surveillance equipment, prolong its autonomy, and lower the required data-storage capacity, an ultra-low-power always-on “wake-up” circuitry can be used [3] to trigger the signal acquisition and storage only in the presence of potential acoustic events of interest. In spite of significant research of always-on acoustic wake-up interfaces for terrestrial applications [4–6], limited work has been done in the context of detection of underwater acoustic events. Even then, most of the research efforts address asynchronous underwater communications by acoustic modems [2].
Here we demonstrate a design of an ultra-low-power acoustic wake-up interface for underwater detection of passing naval vessels (boats, ships) by analyzing the time-frequency signatures [7] of ship’s engine and propeller noise. Our key contributions w.r.t. the previous work [7] include the end-to-end design of a fully-programmable multichannel mixed-signal wake-up circuit, its implementation with state-of-the-art COTS components yielding minimal power consumption, characterization of circuit’s design parameters, and verification of its detection performance on hydrophone recordings of passing ships [8].

2. Materials and Methods

The design entails three analog-domain filtering channels decomposing the hydrophone’s signal ($V_{in}$) into arbitrary frequency bands (Figure 1a). Each channel consists of a digitally programmable active band pass filter. Each filter is designed in general impedance convertor (GIC) topology, and implemented using a pair of single-supply, CMOS, 100 kHz gain-bandwidth, 600 nA amplifiers (MCP6142, Figure 1b). Channel CH1 is designed to cover 200–500 Hz, CH2 for range between 500–1000 Hz, and CH3 1.0–2.5 kHz. Within these limits, each channel’s central frequency ($f_c$) and pass-band bandwidth ($B$), are digitally programmable in 256 steps using I2C potentiometers $R_{fc}, R_b$ (AD5144).

Intervals of signal presence within individual filtering channels ($V_{filt}$) are localized in time first by extracting the envelopes ($V_{env}$) of filtered signals with active envelope trackers (MCP6141), and then performing single-bit quantization using comparators (TLV3701). Each channel’s comparator threshold voltage ($V_{comp}$) is set by a digital potentiometer $R_{tr}$ (AD5144).

Temporal relations between comparator responses ($V_{out}$) are finally analyzed by a 3-channel digital sequence recognition state-machine [7] (MSP430F2013), which wakes-up the DSP. Event’s template—an ordered sequence of discrete spectro-temporal states describing the vessel’s acoustic signature, is programmed into the MSP430F2013 by I2C as well. Consumption of MSP430F2013 is minimized by aggressive frequency scaling (11 kHz) and extensive use of low-power modes (LPM4, LPM3).

A PCB prototype implementation of the described wake-up circuit with proposed COTS components (Figure 2a) was used for experimental characterization of design parameters, testing of detection performance, and power consumption measurements.

![Figure 1. The 3-channel always-on wake-up interface. (a) Architecture; (b) Design of an individual analog channel (GIC filter, active envelope tracker, comparator).](image-url)
Figure 2. Design characterization. (a) PCB; (b) Range of filters’ central frequencies (adjusted by $R_f$), (c) bandwidths (set by $R_b$), and (d) span of comparators’ thresholds ($R_{tr}$); (e) Filters’ gains ($V_{filt}/V_{in}$); (f) Envelope trackers’ sensitivities ($V_{env}$).

3. Results

Results of characterization of the PCB prototype are summarized in Figure 2. First, adjustable ranges of each channel’s programmable parameters are shown in Figure 2b–d. Range of filtering channels’ central frequencies $f_c$ against digital potentiometers $R_f$ are verified in Figure 2b: CH1 200–500 Hz, CH2 500–1100 Hz, and CH3 1.0–2.6 kHz. RB’s are tuned for narrow, 150–200 Hz passbandwidths $B$ at low and mid-frequency channels CH1 and CH2 (Figure 2c), and CH3 is enabled to extract broader bands of 500–800 Hz at the $f_c = 1.0$ kHz, and 200–500 Hz at the maximal $f_c = 2.6$ kHz. $R_{tr}$ enables setting the comparator’s threshold voltage within approximately 50 mV (Figure 2d) around the envelope detector’s output voltage baseline ($V_{env}$), sitting on $V_{ref} = 0.9$ V.
Ideally, gains of GIC filtering stages is 2 (Figure 2e). Frequency-dependent gain increase at the CH3 helps detection of high-frequency signal, typically exhibiting lower power-spectrum density (amplitude) than the signals at low frequencies (CH1, CH2). Sensitivity of the complete analog signal chain was measured at the envelope detector’s output, as a ripple-free headroom between the $V_{\text{env}}$ and the signal’s baseline, floating around $V_{\text{ref}}$ (Figure 2f). $V_{\text{env}}$ ripple affects the sensitivity at low frequencies at most. Sensitivity to input signals as low as $V_{\text{in}} < 5$ mV, enables direct coupling of the wake-up circuit to the sensor.

Event detection performance was tested on underwater recordings of 12 passes of an identical speedboat over an acoustic sensor submerged approx. 1 m under the surface in shallow water [8]. Signals were rescaled to yield max. $V_{\text{in}} = [20…5]$ mV amplitude at pass-over. For each $V_{\text{in}}$ thresholds $R_{tr}$ were trained on a single signal, and then tested on the remaining 11. A spectrogram of a training-pass is shown in Figure 3a. The associated circuit response, including a successful wake-up upon the simultaneous detection of uninterrupted 500 ms signal within all three arbitrarily set bands of interest (CH1 200–350 Hz, CH2 580–720 Hz, and CH3 1460–1920 Hz) is shown in Figure 3b. In the testing-phase, circuit was able to detect 8/11 speedboat passes with the $V_{\text{in}}$ amplitude = 10 mV.

**Figure 3.** An acoustic event detection with the always-on wake-up circuit. (a) A typical underwater spectrogram signature of a passing speedboat [8]; (b) The associated wake-up circuit response ($V_{\text{in}} = 20$ mV). DSP processing may be initiated on the rising edge of the wake-up signal.

Current consumption was measured at 1.8 V. GIC filtering within 2.5 kHz implemented with MCP6142, costs 3.5 µA/channel. Each active envelope tracker contributes by 0.8 µA. Full-programmability, implemented with three digital potentiometers per channel (AD5144), adds 0.3 µA. Each comparator costs 1.8 µA, mostly due to the current flowing through the programmable resistor-divider network defining the $V_{\text{comp}}$. Digital state machine implemented in MSP430F2013 consumes only 0.2 µA in listening [7]. This totals 6.4 µA/channel, or 19.4 µA for the three-channel prototype.
4. Discussion

Comparison with functionally and architecturally related state-of-the-art acoustic wake-up designs is given in Table 1. Consuming only 20 µA, the presented design (Figure 2a) achieves the best tradeoff between functionality (signal analysis capabilities), bandwidth and programmability, among COTS designs [3–5] (Table 1). An analogous ASIC design [6] shows potential for 100-fold power reduction.
Table 1. Current consumption breakdown ($V_{CC} = 1.8$ V), and comparison to the related acoustic wake-up designs. (Legend: - feature not present; feature present, but consumption not reported).

<table>
<thead>
<tr>
<th>Ref.</th>
<th>Techn.</th>
<th>Functionality</th>
<th>Band-Width</th>
<th>Filter/ch.</th>
<th>Envelope Tracking/ch.</th>
<th>1-bit Digitizer/ch.</th>
<th>Program Mability/ch.</th>
<th>Classification</th>
<th>Single Channel</th>
<th>Total (all ch.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>[3]</td>
<td>COTS</td>
<td>Fixed, single ch. Acoustic modem with address decoding</td>
<td>150 kHz</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>4.5 µA</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>this</td>
<td>dig. programmable 3 ch. filtering, 1-bit envelope digit., time-freq. template matching</td>
<td>2.5 kHz</td>
<td>3.5 µA</td>
<td>0.8 µA</td>
<td>1.8 µA</td>
<td>0.3 µA</td>
<td>0.2 µA</td>
<td>6.4 µA</td>
<td>19.4 µA</td>
</tr>
<tr>
<td>[4]</td>
<td>COTS</td>
<td>fixed, single-ch., 1-bit digit., periodicity detection in FPGA</td>
<td>380 Hz</td>
<td>4.2 µA</td>
<td>-</td>
<td>1.1 µA</td>
<td>-</td>
<td>20 µA</td>
<td>-</td>
<td>85 µA</td>
</tr>
<tr>
<td>[5]</td>
<td>COTS</td>
<td>Fixed, single ch., detection by amplitude thresholding</td>
<td>20 kHz</td>
<td>-</td>
<td>-</td>
<td>3.2 µA</td>
<td>-</td>
<td>-</td>
<td>3.2 µA</td>
<td>-</td>
</tr>
<tr>
<td>[6]</td>
<td>ASIC</td>
<td>multi-ch. filtering, env. Peak detect vs amb. noise</td>
<td>100 Hz</td>
<td>0.34 nA</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>4.3 nA</td>
<td>-</td>
</tr>
</tbody>
</table>
5. Conclusions

We demonstrated a prototype of an always-on, multichannel, mixed-signal, fully-programmable wake-up sensor interface, consuming less than 20 µA, while listening for time-frequency signature of the acoustic event. Usage of the proposed circuit for on-demand triggering of the conventional underwater acoustic surveillance equipment [1,2], may reduce its average power consumption for more than two orders of magnitude. Apart from underwater security, diver safety, and marine biology, the circuit is applicable in many terrestrial event detection applications, including acoustic emission monitoring in industry and agriculture.

Acknowledgments: This research has been supported in part by the U.S. Office of Naval Research Global under the project ONRG-NICOP-N62909-17-1-2160, AWAKE – Ultra low power wake-up interfaces for autonomous robotic sensor networks in sea/subsea environments, and partially by Croatian Science Foundation under the project IP-2016-06-8379, SENSIRRIKA – Advanced sensor systems for precision irrigation in karst landscape.

Conflicts of Interest: The authors declare no conflict of interest. The founding sponsors had no role in the design of the study; in the collection, analyses, or interpretation of data; in the writing of the manuscript, and in the decision to publish the results.

References


© 2018 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (http://creativecommons.org/licenses/by/4.0/).