Abstract: This paper constitutes the analysis of the impact of low doped intrinsic p-type EPI thickness (20 µm and 30 µm) and bottom anti-reflective coating on the electrical and optical performance of various PIN photodiodes designs. The intrinsic p-type layer with a target resistivity of 400 Ω cm is an epitaxial layer (iEPI) grown on a low resistive substrate of 20 mΩ cm. Optimization of the photodiode’s spectral responsivity (for a specific wavelength) includes a Bottom Anti-Reflective Coating (BARC) layer deposited over the silicon surface. BARC thickness is optimized for λ = 425 nm, λ = 750 nm and λ = 900 nm wavelengths. With respective BARC in place, the photodiode’s quantum efficiency (QE) approaches 100% for λ = 750 nm with 20 µm and 30 µm iEPI thickness and for λ = 900 nm with 30 µm iEPI reaching also a maximum spectral response of 0.63 A/W at 800 nm. QE of 72% could be achieved at 425 nm. The leakage current varies from 3.5 pA for 20 µm iEPI thickness to 10 pA for 30 µm at 1 V reverse biasing for 365 µm circular PIN photodiode.

Keywords: intrinsic epitaxial substrate; PIN photodiode; spectral responsivity; leakage current; capacitance

1. Introduction

A photodiode is a device based on a p-n junction collecting photocurrent generated in the substrate. The combination of reverse biasing and large depletion area are necessary for improving the quantum efficiency of the photodiode [1] because of the reduction of the transit time of the carriers in the substrate. A way to enlarge the depletion area of the photodiode is the use of low-doped intrinsic material thus forming a PIN photodiode. The low-doped intrinsic material additionally reduces the recombination rate of the photo carriers, thus improving the efficiency of the device. The photodiode can be tuned to be sensitive to specific wavelengths by using appropriate p-n junctions as well as a Bottom Anti-Reflective Coating (BARC) layer of wavelength dependent thickness. The impact of the different intrinsic p-type EPI layer thicknesses and photodiode designs are analyzed with respect to optical and electrical performances of circular and square photodiodes.

2. Materials and Methods

One type of PIN photodiodes [2] is constructed with a shallow highly doped n+ type well in intrinsic p-type EPI (iEPI) (Figure 1a), hereafter called nPD. Another type optimized for blue wavelengths uses deep n-type well with a counter-doped shallow p-type well, thus making the
photodiode less sensible to red and near infrared wavelengths (Figure 1b), hereafter called bluePD. By shortening deep n-type well and substrate, the photocurrent generated in the iEPI recombines and does not contribute to the photocurrent collected by the shallow p-well layer.

![PIN photodiodes schematics where epitaxial intrinsic p-type layer is grown on low resistive substrate, substrate and guard contacts are placed at the silicon surface. (a) illustrates PIN photodiode with an n+ layer used as photocurrent collecting area (nPD). (b) optimized blue photodiode in which the photocurrent collecting area is a shallow p-type well inserted in a deep n-type well electrically connected to substrate (bluePD).](image)

Figure 1. PIN photodiodes schematics where epitaxial intrinsic p-type layer is grown on low resistive substrate, substrate and guard contacts are placed at the silicon surface. (a) illustrates PIN photodiode with an n+ layer used as photocurrent collecting area (nPD). (b) optimized blue photodiode in which the photocurrent collecting area is a shallow p-type well inserted in a deep n-type well electrically connected to substrate (bluePD).

All photodiodes have been processed in an industry standard 350 nm high-voltage CMOS technology. The photodiodes’ layout permits a direct pad access to anodes and cathodes without any signal amplification circuit. Photodiodes’ optical characteristics are measured by guiding light from a tunable light source operating from 400 nm to 900 nm wavelengths through a multimode fiber placed above the photodiodes and measuring generated photocurrent on the different pads.

3. Results

Figure 2a presents the impact of the iEPI layer thicknesses and the BARC layers thicknesses on spectral responsivity (SR) for circular and square nPDs. Larger iEPI thickness increases the spectral response in near infrared wavelengths reaching a responsivity of 0.63 A/W at 800 nm for a 30 µm iEPI thickness while the BARC layer increases the local spectral response at its optimized wavelength. The spectral responsivity reaches 100% of the quantum efficiency for BARC layers optimized for $\lambda = 750$ nm and $\lambda = 900$ nm and 82% for layer optimized for $\lambda = 425$ nm wavelengths.

![Figure 2. (a) iEPI thickness/BARC optimization of spectral responsivity as a function of wavelength for circular and square nPD. (b) Photodiode leakage current versus iEPI thickness and BARC optimization for circular nPDs Ø365 um.](image)

Figure 2. (a) iEPI thickness/BARC optimization of spectral responsivity as a function of wavelength for circular and square nPD. (b) Photodiode leakage current versus iEPI thickness and BARC optimization for circular nPDs Ø365 um.

The leakage current of circular nPD (Figures 2b and 3a,b) is dependent on iEPI thickness, doping concentration, and the distance of photodiode to the guard ring [3]. The increase of the iEPI thickness increases the space charge region width around the pn junction thus increases the leakage current.
The extension of the space charge region is more pronounced when the doping concentration of the EPI is closer to the intrinsic concentration n_i which is confirmed by TCAD simulations shown in Figure 3a. At high bias voltage, we can see an increase of the leakage current due to the proximity of the photodiode guard ring consisting of a pnp substrate contact configuration where the n type contact layer is used for the reduction of photodiode to photodiode electrical and optical crosstalk. This configuration generates a lateral npn bipolar with the photodiode which increases the leakage current of the photodiode. In Figure 3b, we can observe that the leakage current is linearly dependent on the photodiode diameter or perimeter.

![Figure 3](image)

**Figure 3.** (a) TCAD simulation of nPD current magnitudes for various iEPI concentration on two EPI thicknesses. (b) Measured leakage current magnitude as a function of photodiode diameter.

The capacitance of the circular nPD is also dependent on the iEPI thickness and photodiode size (Figure 4a,b). The nPD capacitance is bounded by the iEPI thickness because of the low resistive substrate placed below the iEPI layer that is blocking the extension of the space charge width in depth of the photodiode. The circular photodiodes capacitance is linearly dependent to the surface of the photodiode while the impact of the BARC is limited (Figure 4b).

![Figure 4](image)

**Figure 4.** (a) Impact of scaling on circular nPD in 20 µm and 30 µm iEPI. (b) Evolution of capacitance at 0 V versus area of circular nPD.

In addition to circular PIN photodiodes, square PIN photodiodes with a fixed single photodiode geometry of 200 µm × 200 µm are placed. We also processed arrays of 16 square photodiodes (4 × 4, each 200 µm × 200 µm), 49 square photodiodes (7 × 7) and 121 square photodiodes (11 × 11). The cathodes of all the photodiodes placed in an array are connected together. For arrays, the pnp guard ring is only placed around the array while each blocks have only a p type substrate contact.

Figure 5a shows the capacitance of a single square photodiode with 20 µm and 30 µm iEPI thicknesses. As observed for circular photodiodes, the capacitance is lowered when the iEPI thickness is increased. However, because of the smaller size of the squared photodiode, the space charge region of the photodiode reaches the n contact of the guard ring and thus limits the capacitance of the
photodiode between 0 V and −1.5 V for 30 µm iEPI. This limitation is not observed for arrays. Figure 5b shows the evolution of the capacitance of the square PIN photodiode versus the number of photodiodes placed in an array. The capacitance is varying linearly to the number of block. As each block has a fixed geometry, the capacitance is varying linearly to the surface of the array.

\[ \text{(a) Impact of scaling of square nPDs in 20 µm and 30 µm iEPI on the capacitance. (b) Evolution of capacitance at 0 V versus number of unit cells for square nPDs.} \]

In the case of optimized bluePD (Figure 1b), the impact of iEPI thickness on the spectral responsivity curve is limited due to the internal collection of photocurrent while the application of a BARC layer optimized at \( \lambda = 425 \) nm increases the spectral current collected for short wavelengths (Figure 6a). Because of the internal structure of the bluePD, the leakage current is not dependent on the iEPI thickness and is around 10 pA at a biasing below −1 V for a 150 µm × 150 µm photodiode size designed in a square configuration (Figure 6b).

\[ \text{(a) Spectral responsivity for bluePD as a function of iEPI thickness and BARC optimization. (b) bluePD leakage current versus iEPI thickness and BARC optimization.} \]

4. Summary

In this paper, we have presented the impact of low-doped intrinsic p-type epitaxial (iEPI) thicknesses (20 µm and 30 µm) and bottom antireflection coating (BARC) on the optical (spectral responsivity) and electrical (leakage current and capacitance) performances of various PIN photodiodes. PIN photodiodes (n+ in intrinsic p- epitaxial layer) reach a spectral responsivity of 0.63 A/W and 100% quantum efficiency with the deposition of BARC layers optimized for \( \lambda > 750 \) nm in combination with 30 µm iEPI thickness. The leakage current is larger with thicker iEPI thickness and varies from 3.5 pA (20 µm iEPI) to 10 pA (30 µm iEPI) for a circular PIN photodiode (Ø 365 µm). Their capacitance is reduced for larger iEPI thickness and varies from 0.97 pF (20 µm iEPI) to 1.43 pF (30 µm iEPI). Optimized blue photodiodes are not sensible to iEPI thicknesses and have a leakage current of 10 pA for a 150 µm × 150 µm square photodiode.
References