Inhomogeneities in Battery Packs

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Abstract: The market of accumulator based technologies is changing very fast, especially the electromobility market due to different decisions made to reduce the impact on climate change. In most markets battery packs are used, which means accumulators are connected to form a battery system consisting of, from a few accumulators up to thousands. Research of battery technologies often focuses on single cells, however considering the applications it is important to know the behaviour of different cells in a system. This paper deals with the inhomogeneities which influence the electrical behaviour of the system in simulation and measurement with a special focus on parallel connected cells.

Keywords: lithium-ion battery cell; battery pack; parameter variations; parallel-connected cells; current distribution; battery model; battery simulation

1. Introduction

The energy market is changing to alternative energies and the automobile industry is changing to electric vehicles. Both new markets use battery systems consisting of multiple cells connected in parallel and series. Connecting the cells causes them to interact with each other by means of charge transference. To understand the interactions between cells in a system, measurements and dynamic battery pack simulations have to be carried out. Consequently, the reasons and the consequences of the interactions have to be considered. The reasons are inhomogeneities caused by production differences and/or current/temperature distributions in a battery system which influence the performance of the pack or module. To counter the differences in production an approach for sorting the cells by the internal resistance or nominal capacity before the configuration of the pack was suggested by Fuqiang An et al. [1]. Current distributions in parallel connections needs to be considered [2] also how it effects ageing compared to the ageing of a single cell [3]. The inhomogeneities which lead to these current distributions are the mechanical connections (welding, soldering, pressing) of the cells on to the conductor, the influence of different cell capacities and internal resistances and the position of the plus and minus poles of the battery also has an influence on the system.

Typical battery systems are series connected parallel strings, where the terminals of a parallel string is at the plus and minus poles of the first cell in the string or at the plus pole of the first cell and at the minus pole of the last cell in the string. Using this kind of connection and the assumption that the voltages of the cells are exactly the same in a parallel string, it is possible to minimize the measurement effort to a single current measurement of the whole pack or module and one voltage measurement for each parallel string in the pack. Therefore the current distribution is neglected and the battery management system is unable to identify the consequences for the single cell. Neglecting the current distribution could lead to an inaccurate estimation of different states of the parallel connection since the current through a cell influences the estimated capacity, the impedance parameters and therefore the state of charge, the state of health and the state of available power [4]. It leads to the neglection of the parameter variations of the cells as well, which leads to a variation of the ageing as described by [5].
To analyse and understand the behaviour and consequences of the pack design and cell location, many measurements and simulations could be conducted. Some researches have been conducted. For example Long Zhou, et al. [6], studied parameter variations in battery packs consisting of series connected cells. They varied the initial capacity, the initial SOC, the initial resistance, the initial temperature, the capacity decay, the internal resistance growth, the self-discharge rate and the coulombic efficiency in their simulation and tested two in series connected cells, where each cell was in its own climate chamber. Another work by M. Mathew, et al. [7], considered the replacement of modules in a battery pack for electric vehicles and investigated the influences in the series connected modules. The paper by Gregory L. Plett, et al. [8], investigated the simulation of battery packs in series and parallel connections with different initial conditions.

This paper deals with different positions of the cells in a parallel connection. So cells with outlying nominal capacities and internal resistances are positioned at specific points in the connection to accomplish an expected behaviour of the system. Also, the position of the plus and minus terminals of the pack in the parallel connection are changed. Afterwards the simulations are used to clarify and explain the observed results of the measurements, because in the case of the simulation it is possible to observe a specific inhomogeneity. The following chapters deal with the modeling of the pack, the measurements, its results and the simulation and its results, followed by a conclusion.

2. Battery Pack Modelling

The state of the art in electrical battery pack modeling extends from single cell models extrapolated for packs to single cell models for each cell of the pack. The pack models based on a single cell model differ in how the actual functionality and parameters of the pack model is estimated. There are pack models which aggregate single cell models and their behaviour by simply calculating the pack model parameters based on the configuration of the pack without considering cell variations and thermal imbalance. Other pack models based on single cell models use the information of the capacity and internal resistance variations of the cells to calculate a more specific pack model [9]. Another approach based on single cell models is to identify the parameters directly from a build battery pack [10]. These approaches could consider parameter variations between cells but they do not model each single cell, which means that it is not possible to estimate the performance of a single cell in the pack and its specific state.

To investigate the behaviour of single cells in a pack it is required to model every cell in the pack, depending on the aim of the investigation the model of a single cell has to include an electrical, an ageing or a thermal model. This work investigates the influences of variations of capacities and internal resistances in a short term and therefore an electrical model is needed to simulate the dynamic behaviour resulting from the variations. Bruen et al. [11] suggested a model which is modeling the pack using equivalent circuit models for every cell single cell combined in a state space model for a parallel connection only. Modeling a battery pack in the state space leads to fast simulations without losing too much information. Every single cell is modelled by an equivalent circuit model with an inner resistance and up to \( n \) RC-Elements. The proposed model of Bruen et al. is extended to be able to simulate series connections and different terminal positions as well as it has an additional entry for the terminal voltage of the whole parallel connection.

The cell model used in this paper is shown in Figure 1 and consists of a voltage source for the open circuit voltage, a series resistor for the battery internal resistance and RC parallel elements for the dynamic behaviour.

Its state space representation is shown in Formulas (3) to (4), which is given by [11]. The general form of a state space model is given by Formulas (1) and (2).

\[
\dot{x} = Ax + Bu \quad (1)
\]

\[
y = Cx + Du \quad (2)
\]
\[
\begin{bmatrix}
\dot{SOC} \\
\dot{V}_{p,1} \\
\dot{V}_{p,2}
\end{bmatrix}
= 
\begin{bmatrix}
0 & 0 & 0 \\
0 & -\frac{1}{R_{p,1}C_{p,1}} & 0 \\
0 & 0 & -\frac{1}{R_{p,2}C_{p,2}}
\end{bmatrix}
\begin{bmatrix}
SOC \\
V_{p,1} \\
V_{p,2}
\end{bmatrix}
+ 
\begin{bmatrix}
1 \\
\frac{1}{C_{p,1}V_{p,1}} \\
\frac{1}{C_{p,2}V_{p,2}}
\end{bmatrix}
i_{cell}
\] (3)

\[
\begin{bmatrix}
V_1
\end{bmatrix}
= 
\begin{bmatrix}
\frac{f(SOC)}{SOC} & 1 & 1
\end{bmatrix}
\begin{bmatrix}
SOC \\
V_{p,1} \\
V_{p,2}
\end{bmatrix}
+ 
\begin{bmatrix}
R_D
\end{bmatrix}
i_{cell}
\] (4)

These cells can be connected in different arrangements which leads to different behaviour of the battery pack. They could be arranged in a four series with two parallel strings (i.e., Figure 2a) or with two parallel cells connected four times in series and any other number of cells (i.e., Figure 2b).

**Figure 1.** cell model.

**Figure 2.** The figures show (a) a system of parallel connected series strings and (b) a system of series connected parallel strings.

Often a connection of parallel strings in series is used, because the cells are balancing each other in parallel strings without an additional balancing system. Also the location of the plus and minus poles are important for the behaviour of the system. Using the networks of a parallel string, the meshes and knots are calculated and lead to the matrices A, B, C and D of the state space model. The scheme is given by Bruen et al. [11] and leads to the following state space model, which is adjusted such that the terminal voltage of a parallel cell string could differ from the voltage of the first cell and all cell
voltages are included in the output vector. So the matrices A, B, C and D are mostly diagonal matrices of the matrices of a single cell.

\[
\begin{bmatrix}
0 \\
\dot{x}_1 \\
\dot{x}_2 \\
\vdots \\
\dot{x}_n
\end{bmatrix} =
\begin{bmatrix}
0 & \cdots & 0 \\
A_1 & 0 & \cdots & 0 \\
\vdots & A_2 & \ddots & \vdots \\
0 & \cdots & \cdots & A_n
\end{bmatrix}
\begin{bmatrix}
x_1 \\
x_2 \\
\vdots \\
x_n
\end{bmatrix}
+ 
\begin{bmatrix}
0 \\
B_1 \\
B_2 \\
\vdots \\
B_n
\end{bmatrix}
\begin{bmatrix}
i_{\text{cell}1} \\
i_{\text{cell}2} \\
\vdots \\
i_{\text{cell}n}
\end{bmatrix}
\tag{5}
\]

\[
\begin{bmatrix}
V_{\text{tbp}} \\
V_1 \\
V_2 \\
\vdots \\
V_n
\end{bmatrix} =
\begin{bmatrix}
0 & C_1 & 0 & \cdots & 0 \\
0 & C_1 & 0 & \cdots & 0 \\
\vdots & \cdots & \ddots & \vdots & \vdots \\
0 & \cdots & \cdots & C_n & 0
\end{bmatrix}
\begin{bmatrix}
x_1 \\
x_2 \\
\vdots \\
x_n
\end{bmatrix}
+ 
\begin{bmatrix}
2R_{\text{Con}} + D_1 & 2R_{\text{Con}} & \cdots & 2R_{\text{Con}} \\
D_1 & 0 & \cdots & 0 \\
0 & D_2 & \ddots & \vdots \\
\vdots & \cdots & \ddots & 0 \\
0 & \cdots & 0 & D_n
\end{bmatrix}
\begin{bmatrix}
i_{\text{cell}1} \\
i_{\text{cell}2} \\
\vdots \\
i_{\text{cell}n}
\end{bmatrix}
\tag{6}
\]

The interaction of the parallel connected cells is included in the calculation of the cell currents, which is influenced by the voltages of the cells and the voltage drops over the connection resistances. As shown by Bruen et al. the cell current could be calculated by using the voltage loops for a parallel connection.

\[
0 = \begin{cases} 
V_{\text{OC},n} - V_{\text{OC},n-1} + V_{p,n} - V_{p,n-1} + R_{D,n}(i_n - i_{n+1}) - R_{D,n-1}(i_{n-1} - i_n) + 2R_{\text{Con}}i_n, & n < N \\
V_{\text{OC},n} - V_{\text{OC},n-1} + V_{p,n} - V_{p,n-1} + R_{D,n}i_n - R_{D,n-1}(i_{n-1} - i_n) + 2R_{\text{Con}}i_n, & n = N 
\end{cases}
\]

These voltage loops are then rearranged for a matrix form. This leads to three new matrices, the resistance matrix R, the state dependency matrix E and input dependency matrix F. The result should take the form of Equation (7), where \(i_n\) are the currents through the connection resistances and \(i_1\) is the input current for the parallel connection.

\[
R \begin{bmatrix} i_1 \\ i_2 \\ \vdots \\ i_n \end{bmatrix} = E x + F i_1
\tag{7}
\]

This form and the voltage loops lead to the following matrices.

\[
R = 
\begin{bmatrix}
1 & 0 & 0 & 0 \\
0 & -(R_{D,1} + R_{D,2} + 2R_{\text{Con}}) & R_{D,2} & 0 \\
0 & R_{D,2} & \ddots & R_{D,n-1} \\
0 & 0 & R_{D,n-1} & -(R_{D,n-1} + R_{D,n} + 2R_{\text{Con}})
\end{bmatrix}
\tag{8}
\]
By using the voltage loops in matrix form and inverting the matrix $R$, it is possible to solve Kirchhoff’s Current Law for the cell currents

$$i_{cell,n} = \begin{cases} 
i_n - i_{n+1}, & n < N \\
i_n, & n = N \end{cases}$$

in matrix form. Therefore, the current translation matrix $G$ as described by Bruen et al. is introduced.

$$G = \begin{bmatrix} 1 & -1 & 0 & 0 \\ 0 & 1 & -1 & 0 \\ 0 & \ddots & 1 & -1 \\ 0 & \cdots & 0 & 1 \end{bmatrix}$$

These calculations lead to the new extended state space model

$$\dot{x} = Ax + B \left[ GR^{-1}(Ex + Fi_1) \right]$$

$$y = Cx + D \left[ GR^{-1}(Ex + Fi_1) \right].$$

The model was explained for a parallel string, where the terminals of the parallel strings are at the first cell. The connection of the terminals influences the overall behaviour as well. Different connections of the terminals could be implemented with the model, too, by modifying the $D$ matrix and the calculation of the cell currents, because the location of connection resistances changes the $R$ matrix. The $D$ and the $R$ matrix in case of the terminals being at the plus pole of the first cell and the minus pole of the last cell take the following form.

$$D = \begin{bmatrix} (N + 1)R_{Con} + D_1 & NR_{Con} & \cdots & 2R_{Con} \\
D_1 & 0 & \cdots & 0 \\
0 & D_2 & \ddots & \vdots \\
\vdots & \ddots & \ddots & 0 \\
0 & \cdots & 0 & D_n \end{bmatrix}$$

$$R = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 \\
R_{Con} & -\left(R_{D,1} + R_{D,2} + 2R_{Con}\right) & R_{D,2} & 0 & 0 \\
R_{Con} & R_{D,2} & \ddots & R_{D,n-1} & 0 \\
R_{Con} & 0 & R_{D,n-1} & -\left(R_{D,n-1} + R_{D,n} + 2R_{Con}\right) & 0 \end{bmatrix}$$
To represent battery packs with series connected parallel strings the matrices of the parallel connected cells are concatenated in diagonal matrices to form the whole system. This leads to a model of a battery system with multiple layers consisting of the cell model, parallel string and the whole system. So the concatenation of the parallel strings results also in a diagonal matrix as shown in the following equations, where the index ps indicates one parallel string. The matrices to calculate the cell currents are concatenated in the same manner.

\[
\begin{bmatrix}
\dot{x}_{ps1} \\
\dot{x}_{ps2} \\
\vdots \\
\dot{x}_{psn}
\end{bmatrix}
= \begin{bmatrix}
A_{ps1} & & & 0 \\
& A_{ps2} & & \vdots \\
& & \ddots & \\
0 & & \cdots & C_{psn}
\end{bmatrix}
\begin{bmatrix}
x_{ps1} \\
x_{ps2} \\
\vdots \\
x_{psn}
\end{bmatrix}
+ \begin{bmatrix}
B_{ps1} \\
B_{ps2} \\
\vdots \\
B_{psn}
\end{bmatrix}
\begin{bmatrix}
0 \\
D_{ps1} \\
\vdots \\
D_{psn}
\end{bmatrix}
\begin{bmatrix}
0 \\
0 \\
\vdots \\
0
\end{bmatrix}
\begin{bmatrix}
i_{ps1} \\
i_{ps2} \\
\vdots \\
i_{psn}
\end{bmatrix}
\]

(16)

\[
\begin{bmatrix}
V_{ps1} \\
V_{ps2} \\
\vdots \\
V_{psn}
\end{bmatrix}
= \begin{bmatrix}
0 & C_{ps2} & \cdots & 0 \\
0 & 0 & \ddots & \vdots \\
\vdots & \ddots & \ddots & 0 \\
0 & \cdots & 0 & C_{psn}
\end{bmatrix}
\begin{bmatrix}
x_{ps1} \\
x_{ps2} \\
\vdots \\
x_{psn}
\end{bmatrix}
+ \begin{bmatrix}
0 \\
0 \\
\vdots \\
0
\end{bmatrix}
\begin{bmatrix}
0 \\
D_{ps1} \\
\vdots \\
D_{psn}
\end{bmatrix}
\begin{bmatrix}
0 \\
0 \\
\vdots \\
0
\end{bmatrix}
\begin{bmatrix}
0 \\
0 \\
\vdots \\
0
\end{bmatrix}
\begin{bmatrix}
i_{ps1} \\
i_{ps2} \\
\vdots \\
i_{psn}
\end{bmatrix}
\]

(17)

Characterization and Parameterization

The described model had to be parameterized and therefore the cells have to be characterized and tested to generate the profiles to parameterize the battery model. By conducting a quasi open circuit voltage (QOCV) measurement the capacity could be calculated and the measurement result could be used as characteristic map for the cell models. The cells were at first discharged and then charged with a current of \(C/20\). Figure 3 depicts some example results of the QOCV measurement.

![Figure 3. QOCV results both charging and discharging of two of the LGHE4 cells.](image)

The measurements show variations in the internal resistance and the capacity (see Table 1). There is also one outlier when the QOCV is considered. At this point it has to be mentioned that the cells are all new and appear to exhibit production quality variations, perhaps even flaws. This is a perfect example of how production tolerances can vary. Each battery system can suffer under the production tolerances of the single cells, which lead to slightly different capacities and internal resistances. To parameterize the cell models additional tests were carried out. The cells were charged and
discharged in 10% steps of the state of charge (SOC). After the charge or discharge steps there were two hours of relaxation. These relaxations of the voltage were used to identify the parameters for a cell model consisting of an internal resistance and two RC parallel elements. An example pulsed discharge profile is depicted in Figure 4. The relaxation parts were extracted and fitted using a Nelder-Mead algorithm to identify the models parameters.

\[ R_D = \frac{\Delta V}{\Delta I} \]  

Figure 4. current and voltage measured during the pulsed discharge for the parameterization of the model.

To accomplish a successful fitting, the start-parameters of the fittings were calculated for each relaxation by identifying the different areas for the internal resistance and the RC parallel elements. The internal resistances is directly calculated from the relationship

where the voltage and current difference was calculated after about 20 ms after the start of the relaxation. Because of the kind of the internal resistance calculation the internal resistance represents parts of the charge transfer resistance. By subtracting the voltage drop across the internal resistance and the OCV the remaining voltage is the voltage across the RC parallel elements. Thus the voltage was divided into different parts which should be represented by the RCs. Therefore the resistances can be calculated and the time constant can be determined. Table 1 sums up the mean internal resistances across the different SOCs and capacities of the 8 cells. Now there are only the connection resistances left to complete the model. The connection parameters where measured using a HIOKI BT3554 Battery Tester which applies a 2 kHz sinussoidal current to conduct an impedance measurement. The resistances for the connections are about 2 mΩ.

<table>
<thead>
<tr>
<th>Cell ID</th>
<th>( C_{\text{act}} ) [Ah]</th>
<th>( R_0 ) [Ω]</th>
</tr>
</thead>
<tbody>
<tr>
<td>LGHE4-1</td>
<td>2.51</td>
<td>0.036</td>
</tr>
<tr>
<td>LGHE4-2</td>
<td>2.55</td>
<td>0.038</td>
</tr>
<tr>
<td>LGHE4-3</td>
<td>2.49</td>
<td>0.03</td>
</tr>
<tr>
<td>LGHE4-4</td>
<td>2.49</td>
<td>0.05</td>
</tr>
<tr>
<td>VTC5-1</td>
<td>2.68</td>
<td>0.029</td>
</tr>
<tr>
<td>VTC5-2</td>
<td>2.68</td>
<td>0.029</td>
</tr>
<tr>
<td>VTC5-3</td>
<td>2.74</td>
<td>0.029</td>
</tr>
<tr>
<td>VTC5-4</td>
<td>2.69</td>
<td>0.039</td>
</tr>
</tbody>
</table>

The parameters were fitted for charging and discharging direction and were averaged for each 10% SOC step. Also, the QOCV were averaged for a mean open circuit voltage.
Figure 5 depicts a comparison of the model with the measurement. It shows a part of the profile, which is used in the measurement section. Also, the setup is described in that section. The model consists of four parallel connected cells. The currents show a similar behaviour, where the voltages have higher errors. In the model the currents are calculated from the voltages in a voltage loop, therefore small errors in the current should lead to small errors in the voltages, if the parameters are a good estimation of the real battery parameters, because they influence the calculation of the current distribution. The simplifications, like using mean parameters and quasi open circuit voltages, and a maybe flawed characterization could have led to these errors. However, the model seems to emulate the overall behaviour sufficiently. More time could be spent to improve the parameterization of the pack model, but at this point the models’ performance is adequate for amplifying the goal of the work, to investigate the influence of parameter variations.

3. Measurement and Simulation

Many applications use battery systems, which consist of parallel and series connected cells. The majority of research deals with variations in series connections, where the parallel connected cells are considered equal [5–7]. This paper concentrates on parallel connections and the influence of inhomogeneities. A few different measurements were conducted, where the cells have different capacities, internal resistances and different connections. Due to the concentration on parallel connected cells the measurement setup consists of four parallel connected cells where the current in each path and each temperature is measured. The setup is variable and the interconnections could be changed easily due to the usage of screwed contacts. Figure 6a,b depict the setup as overview of the printed circuit board and as simplified circuit diagram including the different terminal positions. For the measurements two different high power 18,650 cells were used. One cell is the LGHE4 from LG with a nominal capacity (\(C_{\text{nom}}\)) of 2.5 Ah and the other is the VTC5 from Sony with \(C_{\text{nom}} = 2.6\) Ah. They have slightly different internal resistances as well, see Table 1.
3.1. Measurement Cases

The measurements belonging to the parallel connections consider different capacities, internal resistances and different interconnections of the cells, which means that the terminals of the pack are at different locations of the pack. These cases lead to five different measurements.

Test 1 The first test uses only the cells from LG in the order LGHE4-1, LGHE4-2, LGHE4-3 and as fourth cell the LGHE4-4. The terminals were connected to the poles of the first cell in the configuration. This test should highlight a very poor scenario, where the fourth cell experiences the smallest load due to its internal resistance and its location.

Test 2 The second test changes the positioning of the cells in comparison to test one. LGHE4-4 was placed at position two in the parallel connection, where LGHE4-2 and LGHE4-3 are shifted to position three and four. The terminals were connected to the poles of the first cell in the configuration. This test should show how the location of a higher internal resistance influences the current distribution.

Test 3 Test three uses three Sony cells at position one (VTC5-1), three (VTC5-2) and four (VTC5-4). At position two is an LG cell (LGHE4-2). The terminals were connected to the poles of the first cell in the configuration. This test shall display the effect of a smaller nominal capacity in a parallel connection.

Test 4 LGHE4-1, LGHE4-2 and LGHE4-3 were used as cell one, cell three, and cell four respectively. The second cell in the measurement setup was VTC5-3. The terminals are located at the plus and minus poles of the first cell. In comparison to test three, test four shows the influences of a higher nominal capacity.

Test 5 During this test the terminals plus and minus of the pack were connected to the first and last cell respectively. The cell configuration is the same as in test one. This test in comparison with test one is used to investigate the influence of the terminal location.

Each measurement is conducted using the same current profile. The cells start at 4 V and are discharged with a constant current of 4 A with intermitting charge and discharge pulses of 8 A until the terminal voltage of the pack reaches 3.4 V. Afterwards the pack is charged with the constant current constant voltage scheme to 4 V. The operation window is for safety reasons between 3.4 V and 4 V. Table 2 sums up the measurement configurations.
Table 2. Configuration of the parallel connection during the measurements.

<table>
<thead>
<tr>
<th>Measurement 1</th>
<th>Measurement 2</th>
<th>Measurement 3</th>
<th>Measurement 4</th>
<th>Measurement 5</th>
</tr>
</thead>
<tbody>
<tr>
<td>LGHE4-1</td>
<td>Cell 1</td>
<td>Cell 1</td>
<td>Cell 1</td>
<td>Cell 1</td>
</tr>
<tr>
<td>LGHE4-2</td>
<td>Cell 2</td>
<td>Cell 3</td>
<td>Cell 2</td>
<td>Cell 2</td>
</tr>
<tr>
<td>LGHE4-3</td>
<td>Cell 3</td>
<td>Cell 4</td>
<td>Cell 4</td>
<td>Cell 3</td>
</tr>
<tr>
<td>LGHE4-4</td>
<td>Cell 4</td>
<td>Cell 2</td>
<td>Cell 4</td>
<td>Cell 4</td>
</tr>
<tr>
<td>VTC5-1</td>
<td>Cell 1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VTC5-2</td>
<td></td>
<td>Cell 3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>VTC5-3</td>
<td></td>
<td></td>
<td>Cell 2</td>
<td></td>
</tr>
<tr>
<td>VTC5-4</td>
<td></td>
<td></td>
<td></td>
<td>Cell 4</td>
</tr>
<tr>
<td>Terminal</td>
<td>+/- 1st Cell</td>
<td>+/- 1st Cell</td>
<td>+/- 1st Cell</td>
<td>1st-last Cell</td>
</tr>
</tbody>
</table>

Figure 7 depicts the voltages of the cells and the terminal as well as the total current through the parallel connection. It exemplifies the current profile which is used to investigate the variation of parameters in parallel connections. As it is shown in the figure the voltage differs by up to 200 mV and further comparisons are conducted for a small excerpt of the current profile, which is marked in Figure 7.

![Figure 7: voltages of the cells and the pack current during case 1.](image)

The expectations of a specific behaviour for each of the tests is driven by the configuration and the parameters of the cells and the connections. Figure 8 displays a part of the testing scheme for every measurement.

In the first test the capacities of the cells lie in the range of 2.49 Ah and 2.55 Ah. Whereas the internal resistances have a higher discrepancy of up to 20 mΩ. The order of the internal resistances is 36 mΩ, 38 mΩ, 30 mΩ and 50 mΩ. By considering the internal resistances on its own, the highest current would be expected for cell three, which is the cell with the lowest internal resistance. For the cell with the highest internal resistance the current should be the smallest. When the connection resistances and position of the terminals are taken into account, the first cell should be the cell with the highest current in the parallel connection and the last cell with the smallest current. Combining the expectations leads to the assumption that cell four would experience by far the smallest current followed by cell three, two and one. Figure 8 shows an excerpt of test one, which confirms the assumption. The currents and the voltages of the first three cells lie in the range of 1 Ah and 50 mV, whereas the fourth cell experiences differences of more than 50 mV and 1 A.

By changing the position of the cell with the highest internal resistance, it is expected that the current distribution would change accordingly. This case is investigated in test two, where the fourth
cell of test one (LGHE4-4, 50 mΩ) is placed at the second position and the other two cells (LGHE4-2, LGHE4-3) are shifted to the position three and four. So the assumption is that the current load for the second cell decreases, where it increases for the fourth cell due to the smaller internal resistance. The results show that this assumption is not met, since the current load for the fourth cell decreases, the load for the second cell stays the same and the third cell, which had in test one a smaller resistance is now loaded with a higher current than before. So the main conclusion from this test is that the positioning has a higher influence than the actual internal resistance of the cell at a specific position, with this specific measurement setup.

Test three investigates the case of having a cell with a smaller nominal capacity in the parallel connection. So the configuration consists of three VTC5 cells, VTC5-1, -2 and -4 at positions one, three and four. At position two is the LGHE4-2 which has about 130 mAh lower capacity than the VTC5 cells and an 10 mΩ higher internal resistance except VTC5-4. A lower capacity should lead especially at a low or high state of charge to an influence on the open circuit voltage due to the exponential change of the open circuit voltage. Due to the internal resistances and the positions in this configuration, cells two and four are expected to be loaded to a smaller amount than before. This configuration leads to an equal current during the high charging current for cell two and three, so cell two is loaded less considering its position in the configuration, but that could not be observed during the discharges, which could be a result of variations in the charging and discharging behaviour. The slightly lower capacity leads to a faster decrease of the voltage of cell two in the section of the smaller discharging current. So the predetermined current distribution caused by the position could be influenced by the arrangement of the internal resistances and lower capacities could influence the current distribution as well, especially at a lower state of charge.

Where test three investigated the case of a smaller capacity in a parallel connection test four investigates the case of a higher capacity. To be able to investigate the given case a connection of LGHE4-1 (2.51 Ah), VTC5-3 (2.74 Ah), LGHE4-2 (2.55 Ah) and LGHE4-3 (2.49 Ah) was configured. The internal resistances lie in the range of 10 mΩ (0.036 Ω, 0.029 Ω, 0.038 Ω and 0.03 Ω for cell one to four in the configuration). Due to the positioning and the internal resistances cell two could be loaded more than would normally be expected in this position. The higher capacity could influence the current distribution if the other cells reach the exponential part of their open circuit voltage. During the test this case was not reached and the assumption that cell two has a higher load considering its internal resistance and its position could not be observed. In this case the voltages of cells one to three are nearly the same, just cell four shows a highly differing voltage, which leads to the assumption that the connection resistances are high, but the measurements of the connection resistances do not support that assumption.

In test five the influence of the location of the terminals of the parallel connection should be analysed. The configuration is exactly the same as in test one except the location of the terminals, where the positive terminal is connected to the positive pole of the first cell and the negative terminal is connected to the negative pole of the last cell. This leads to the expectation that the first and the last cell will be loaded the most, due to their position, but the fourth should be loaded less, due to its internal resistance. The measurement revealed a shift of the current distribution, such that the second cell is loaded like the first cell, and the fourth cell is loaded even less than in test one. So the terminal location does influence the current distribution but in this setup not as expected.

The different cases have shown that different internal resistances and capacities have different influences on the behaviour in a parallel connected string of cells. They lead to higher currents or smaller currents. But in a real measurement the expectations could not be observed. The main reasons are that in every real setup the cells are not exactly the same, so they have different internal resistances and capacities. They could have different open circuit voltages as well. Because the expectations depend on a mean value of internal resistances and capacities, there could also be false assumption, because the charging and discharging internal resistance could differ. Also the terminal location did
influence the behaviour, but the results were not as expected. To identify the effects of the configuration of a parallel connection and their source, simulations are a helpful tool.

![Figure 8. Voltages and currents of one part of the different tests.](image)

### 3.2. Simulations

The simulations are used to reason the expectations and to understand the influence. Simulations bring the advantage that it is possible to have four identical cells as defined by their parameters. Also the connection resistances can be neglected to investigate the influence of different internal resistances and capacities without the current distribution due to the terminal location or other influences. Three simulations were conducted to show the behaviour of a parallel connection under specific conditions without disturbing other influences.

**Simulation 1** This simulation shows the behaviour of parallel connection when there is one cell with a higher resistance present. Every cell has the same parameters of the LGHE4-1 except cell four which is parameterized with the parameters of the LGHE4-4 (0.05 Ω). The connection resistances are very low and the terminals were at the first cell in this simulation.

**Simulation 2** By using the parameters of LGHE4-1 for every cell except cell four which has the same impedance parameters but a nominal capacity of 2.75 Ah, the simulation shows the influence of a higher capacity in the parallel connection. The terminals were simulated at the first cell.

**Simulation 3** In this simulation the cells have the parameters of the cells of test one in the measurements, but they were arranged such that the cells with higher internal resistances are positioned at the outer most locations and the terminals were connected to the positive pole of the first and the negative pole of the last cell. The parameters of the cells positioned at cell one to four are from LGHE4-4, LGHE4-1, LGHE4-3 and LGHE4-2, respectively.

As Figure 9 shows the specific simulations it depicts the effects with no interference of other influences. From simulation one it can be observed that the lower internal resistance leads to the expected smaller load in comparison with the other cells which experience the same load. In the
case of simulation two, the influence of a 10% higher nominal capacity is marginal, because there are very small differences in the current and the voltage. But very small differences in the voltage mean, with almost zero connection resistances, that the fourth cell with the higher capacity is at the same state of charge such that the open circuit voltages are equal, which means that the fourth cell was discharged more during the whole simulation. The last simulation depicts a nearly optimal configuration of a parallel connection of four different cells, which are located in a way that the currents are distributed more equal than in test one. Cells with high internal resistances are positioned at the outer most locations to counter the current distribution due to the positioning in a parallel connection with the terminals at the positive pole at the first and the negative at the fourth cell. Due to the terminals’ location the current distribution by the position in the connection is symmetric, higher loads for the cells connected to the terminals and smaller loads for the cells in between.

![Figure 9. Voltages and currents of one part of the different simulations.](image)

4. Conclusions

The simulation and the measurement of the electrical behaviour of cells in a battery systems is essential to design adequate battery management systems. The model proposed is capable of simulating each cell in a pack with different degrees of dynamics, varying cell behaviour, different interconnections and variable connection resistances. In future work it will be extended with a modular thermal model of a battery pack and an ageing model for the given pack, also taking the ageing of connections due to corrosion into account.

The measurements and simulations dealing with the different possible inhomogeneities of varying internal resistances, capacities and connections were conducted on parallel connections, because a battery management system monitors the cells in series connections. But in parallel connections it is not monitored due to the assumption that the voltages of the cells are equal. Having no connection resistances between the cells leads to this assumption and ensures that the cells are balancing themselves, without additional hardware. In this case the cells could be in different states as well, when the impedance parameters and therefore the overvoltages counter the difference in the state of charge. It is also the case that there are balancing currents between the cells when there are connection resistances which are leading to differing voltages between the cells, but there remains a small offset in the cell voltage corresponding to the resistances of the connections which could
lead to higher differences in the state of a cell. As shown in the measurements and explicitly in the simulations higher internal resistances lead to less current through the cell. Unequal capacities lead to differing charge windows. The charge taken from a cell with a higher nominal capacity would be higher and this means that this cells was discharged with a higher current. One way to compensate for the offset in the state of charge and the unbalanced distribution of the current is to adapt the wiring of the pack terminals as it is shown in simulation three. But still there are imbalances due to the other influences like the internal resistances and nominal capacities. The current distributions lead to different loads of the cells and to different temperatures of the cells which in turn will influence the current distribution. So a cell at the nearest location to the terminals and a smaller internal resistance than the other cells will experience a higher load, as shown by the measurements and the simulations, which leads to a faster increasing temperature. A rising temperature leads in fact to a smaller internal resistance and a faster reaction rate due to the Arrhenius equation and this leads to a higher load for this cell up to the point where the voltages of the cell next to this highly loaded cell and the connection resistances reach the same voltage as the cell next to the terminals. Higher currents lead to increasing temperatures which lead to faster ageing processes. So a sophisticated battery system should use a parallel connection of series connected cells, because in this case every cell is going to be monitored by the battery management system and every state could be traced. A sophisticated but cheaper battery system due to less measurement equipment should consist of well sorted cells and predefined hardware connections with connection and welding resistances as small as possible or it should use symmetric connections for the cells connected in parallel, which could lead to higher costs.

5. Future Work

The upcoming research deals with the extension of the simulation by a temperature and an ageing model. A pack model utilizing electrochemical models for the cells is also a branch of the research. For the validation of the models a modular battery is designed, where every cell could be disconnected to run characterizations for ageing tests with a complete battery pack. Also the locations of the terminals should be interchangeable. Further extensions of the simulation will include battery management functionalities.

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Abbreviations

The following abbreviations are used in this manuscript:

QOCV Quasi open circuit voltage

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